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Analysis of Series Resistance's (R_s) Impact on Ag/Perylene/n-Si Schottky Barrier Diode (SBD) in Various Techniques

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Keywords	Abstract
Ag/Perylene/n-Si SBD	The purpose of this research is to experimentally examine how R_s affects the I-V curves of
Series Resistance	<i>Ag/Perylene/n-Si</i> SBD. Various plots of the experimental I-V measurements with the forward voltage are wielded in order to determine the parameter R _s . The I-V properties of <i>Ag/Perylene/n-Si</i> SBD was
Ohm's Law	evaluated at room temperature (RT) based on Thermionic-Emission (TE) model. We specified the $R_{s} \label{eq:rescaled}$
Modified Norde Function	values using Ohm law, Cheungs', and modified Norde functions. We compared the R_s values utilized various techniques. Modified Norde functions apply to the ln I-V graph's all forward voltage region. On the other hand, Cheung's approaches are just feasible in the non-linear section in the high voltage region.
Cheungs' Methods	The R_s values obtained from various techniques are distinct and are dedicated in the table. The reason for this inconsistency is shown in our research. It is evident that the values of the R_s determined using various approaches are in good accordance with one another. The Ohm's law derived from sufficiently high forward voltages is the one among them that is the most straightforward, precise, and dependable. It was demonstrated by the I-V data that the dispersion of R_s is a key factor affecting the electrical properties of diodes.

Cite

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1. INTRODUCTION

Schottky barrier diodes, which are significant electronic devices in semiconductor technology, play a major role in metal-semiconductor technology because they feature an interface layer made of an insulator, ferroelectric, polymer, or composite materials. Metal-semiconductor-type SBDs transform into MIS/MFS/MPS-kind of SBDs when an interface is formed among metal and semiconductors. But, the interlayer is higher than at about 4-5 hundred nanometers, this structures act a capacitor which storage more electrons or energy (Sze & Ng, 2007; Gencer Imer et al., 2019; Altındal et al., 2022). However, the used an interlayer can be considerably affect both the basic electrical parameters and conduction mechanism of these a structures. The performance or quality of these devices also depends on many parameters, such as the R_s, shunt-resistances (R_{sh}), barrier inhomogeneity, interface thickness and homogeneity, surface states (N_{ss}), and high-voltage (Norde, 1979; Cheung & Cheung, 1986; Sze & Ng, 2007; Rajagopal Reddy et al., 2014). According to Zeyrek et al. (2013) and Okur et al. (2019) the electrical features of these structures can be altered by using interfacial layers such as perylene, and DNA.

The prominence of MPS-type SBDs has increased because they have features like easy growth on the semiconductor, flexibility, and low cost in the last three decades (Sağlam et al., 2004; Yakuphanoglu, 2007; Özden et al., 2022). Among the conductive-polymers, the more important ones are the polythiophene, polyaniline, polyacetylene, PPy, and perylene because of they can easy to synthesize by chemical or electrochemical oxidation (Greene, 2009; Zeyrek et al., 2013). Therefore, we intended to build *Ag/Perylene/n*-

Si MPS-type SBDs rather than traditional MS or MIS. Then utilizing the I-V plots at RT, the electronic parameters n, Φ_B , R_s , R_{sh} , and rectification ratio (RR) was determined. Three linear regions with varying intercept points and slopes are depicted on the experimental I-V plot and correspond to low, moderate, and higher voltages. The I-V curve in the forward voltage shows the model of a multiple parallel diode.

2. MATERIAL AND METHOD

Ag/Perylene/n-Si MPS-type SBD was produced onto the n type Si. Before the processing, it was cleaned in trichloroethylene and ethanol, etched by CP4 (HNO3: HF: COOH C2H5: H2O =3:1:2:2 weight ratio) solution 30 s rinsed by propylene glycol. Following that, it washed with deionised-water (DW) and desiccated with N₂ gas. The wafer form tempered at 400°C to provide decent ohmic-contact. Here, Al was used as ohmic contact and Ag was used as rectifies contact. 1.26 mg of Perylene of 99% purity, branded Alfa Aesar, code L03047, was taken. It was mixed in 10 ml of Sigma-Aldrich brand 34856 coded Dichloromethane at 30°C for 2 hours on a magnetic stirrer. 0.5 mM (millimolar) solution was prepared. Afterward, Si wafers with ohmic contacts were coated with a layer of perylene using the spin coating method. The coating process was carried out at a rotation speed of 2000 rpm for 2 minutes. The processed was completed by thermally growing the rectifier contact on perylene at 10^{-6} Torr. The 3D structure diagram of perylene and schematic diagram of the sample were given in Figure 1. The measurements were realized at RT using Keithley-2400 SourceMeter.



Figure 1. a) The 3D structure diagram of perylene (C₂₀H₁₂) (Zeyrek et al., 2013),
b) The schematic illustration of the SBD.

3. RESULTS AND DISCUSSION

The I-V curves of *Ag/Perylene/n-Si* SBD results examined at RT are shown in Figure 2. The I-V curve of the forward voltage increases exponentially. Furthermore, as illustrated in the inset of Figure 2, the I-V curve of the forward voltage has three different linear regions with distinct slopes, called region-1, 2 and 3 respectively. These multi-linear regions are referred to as the multi parallel diode model in the literature (Güçlü et al., 2016).

The rectification rates (RR=I_{Forward}/I_{Reverse}) were calculated in 0.5 V steps from \pm 0.5 V to \pm 5 V and presented in Figure 3. The RR at V= \pm 0.5 V was even less than 1 (5.25x10⁻²) (Figure 3), and especially reaching ~10³ for \pm 5 V. This indicates that the *Ag/Perylene/n-Si* SBD could also be defined as a good diode as well as has being a high RR, good ohmic performance. Many investigations have demonstrated that a high rate of rectification is achieved by reducing the leakage current. Besides, it is also cognized that the RR, a crucial factor in diode efficiency, is affected by the interface quality and the impurities in the SiO₂/Si substrate. RRs are one of the most features for metal-semiconductor structures in electronic device technologies (Jaffery et al., 2020).

The I-V curve shows a linear behaviour but divergence from linearity owing to the impact of R_s under forward bias. The fundamental diode parameters such as n, Φ_{Bo} , I_o were derived using the TE equations for whole three regions based on the findings of I-V plot (Sze & Ng, 2007).

$$I_{t} = I_{01} \left[\exp\left(\frac{\beta(V - IR_{s})}{n_{1}}\right) - 1 \right] + I_{02} \left[\exp\left(\frac{\beta(V - IR_{s})}{n_{2}}\right) - 1 \right] + I_{03} \left[\exp\left(\frac{\beta(V - IR_{s})}{n_{3}}\right) - 1 \right] + \left(\frac{V - IR_{s}}{R_{sh}}\right)$$
(1)

$$n_i = \beta \left(\frac{dV}{d(lnI)} \right) \tag{2}$$

$$\Phi_{B0i} = \frac{1}{\beta} ln \left[\frac{AA^*T^2}{I_0} \right]$$
(3)

where β is a variable that changes with temperature and is calculated using the equation β =q/kT (q, k, and T are well-known values in the literature). n, A, A*, and Φ_{Bo} are the ideality factor, contact-area of the diode, Richardson-constant of the semiconductor (for n-Si, it is equal to 112 A/(cm².K²), and zero-bias BH, respectively. I₀ is the saturation current, and I·R_s denotes the voltage drop across the series resistor. The n_i, I_{0i}, and Φ_{Boi} values were found from the slope and intercept of the linear part ln(I)-V plot for three linear regions as 5.947, 4.37x10⁻⁷ A, 0.661 eV (for Region-I), 8.433, 4.64x10⁻⁶ A, 0.600 eV (for Region-II), and 7.593, 2.00x10⁻⁶ A, 0.621 eV (for Region-III), respectively. It is clear that these parameters are different in different voltage regions due to the voltage dependent of them.



Figure 2. I-V graph of the Ag/Perylene/n-Si SBD (inset the Region I, II, and III)

Ohm's law ($R_i=V_i/I_i$) was used to calculate the measurements due to the significant influence of R_s . I-V plots for the samples were used and the results were shown in Figure 4. R_i values are constant in both the high forward and high reverse bias regions, and R_i corresponds to R_{sh} and R_s values in these regions. The R_{sh} and R_s values were determined to be 18.45 k Ω and 8.347 Ω at (-5 V) and (+5V) voltage values, respectively.



Figure 3. Rectification Rates of the Ag/Perylene/n-Si SBD



Figure 4. Resistance values of the Ag/Perylene/n-Si SBD as a voltage dependent

The R_s values are one of the major parameters influencing the electrical characteristics of *Ag/Perylene/n-Si* SBD. R_s is the significant source of minor signal loss in semiconductor devices. R_{sh} has a high value, making it ideal for an electronic device. R_{sh} could be caused by leak currents across the insulator among the front and rear contacts. Furthermore, R_{sh} might be caused by the shunt routes from the prob cable to the grounding, as well as the shunt resistance in the measurement device. The R_s is more influential in regions with sufficient forward voltage, whereas the R_{sh} is more potent in regions with reverse bias.

n and Φ_B values be determined only from the rectilinear section of the forward voltage region with TE theory. It is necessary to determine these parameters for the other bias region as well. The Cheungs' and modified Norde's functions can be used to do these computations in other regions (Norde, 1979; Bohlin, 1986; Cheung & Cheung, 1986).

Cheung's methods allow us to calculate the parameters n, Φ_B , and R_S in the region of descending curve of the I_F-V_F graphs. The n, Φ_{B0} , and R_s values are computed utilizing the given equations and presented in Table 1.

$$\frac{dV}{dln(I)} = \frac{nkT}{q} + R_s I \tag{4a}$$

$$H(I) = V - \frac{nkT}{q} \ln\left[\frac{I}{T^2 A^* A}\right] = R_s I + \Phi_{B0} n$$
(4b)

Table 1. Various electronic parameters computed with diverce approaches from the I-V plot at RT

TE Theory (For Degion III)		Ohm's Law		Norda's Eurotion		Cheungs'-Methods					
TE Theory (For Region-III)		Onm's-Law		Norde's-Function		dV/dln (I)		H(I)			
n	I ₀ (A)	Ф _{в0} (eV)	RR at ± 5 V	R _s at +5V (Ω)	$\begin{array}{c} R_{sh} at \text{-}5V \\ (k\Omega) \end{array}$	Ф _{в0} (eV)	R _s (Ω)	n	R _s (Ω)	Ф _{в0} (eV)	R _s (Ω)
7.59	2.00x10 ⁻⁶	0.621	2.21x10 ³	8.34	18.45	0.739	0.541	24.17	4.094	0.415	3.814

The Cheung functions determined as dV/dLn(I) which is known Cheung's 1st function and H(I) which is known as Cheung's 2nd function were plotted versus current. From Cheung's 1st functions. slope and intercept, it is possible to determine the n and R_s of a diode. Figure 5 and Eq. 4a are referred for this. Similarly, the slope and intercept of Cheung's 2nd functions may be used to derive the Φ_{B0} and R_s of a diode using Figure 6 and Eq. 4b. According to Cheung's 1st and Cheung's 2nd functions, the values of n, Φ_{B0} , and R_s were worked out, Table 1 shows them.

The results in Table 1, Figures 5 and 6 indicate that the R_S values acquired Cheung's methods concur. The calculated R_s values using the H(I)- I and dV/d(ln I)- I graphs demonstrate the Cheung method's consistency.

The modified Norde Function is an alternate approach for computing values such as R_s and Φ_B . The modified Norde function considers all forward voltage region of the curve, in which the current changes exponentially. The conventional TE method was utilized to determine the ideality factor employed in the R_s value computation. The Φ_{B0} and R_s values may be computed using the below formulas according to this function.

$$F(V) = \frac{V}{\gamma} - \frac{kT}{q} \ln\left(\frac{I(V)}{AA^*T^2}\right)$$
(5a)

$$\Phi_{B0} = F(V_{min}) + \frac{V_{min}}{\gamma} - \frac{kT}{q}$$
(5b)

$$R_S = \frac{kT(\gamma - n)}{qI_{min}}$$
(5c)

In Eq. 5a, F(V) and γ is an arbitrary constant and must be greater than n, which is derived from Eq. 2, respectively. In Eq. 5b and 5c, I_{min} is the current value matching the F(V)'s minimum. Likewise, V_{min} is the voltage value matching the F(V)'s minimum. The values of R_S and Φ_B were figured out by substituting the values of F (V_{min}), V_{min} , and I_{min} corresponding to F (V), V, and I values of the determined minimum F (V)-V graph (Figure 7), in Eq. 5b and 5c, and recorded in Table 1. The values of $F(V_{min})$, V_{min} , I_{min} was found as 0.593 V, 1.97 V, and 0.0463 A. Thus, the values of and Φ_B and Rs were calculated from Eq. 5b and 5c as 0.740 eV and 0.541 Ω by using the value of γ (1.5*n=11.385).



Figure 5. The dV/dln(I)-I plot of Ag/Perylene/n-Si SBD



Figure 6. The H(I)-I plot of Ag/Perylene/n-Si SBD

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Figure 7. F(V)-V plot for Ag/Perylene/n-Si SBD

It is seen that the R_s values in Table 1 determined using *Ohm law*, *Cheung's methods* and *modified Norde function* are distinct from each other (Turut et al., 2020). These approaches relate to various voltage region, which explains why the results obtained using these methods varies. The TE theory places emphasis on the linear portion, while Cheung's methods center on the downward curvature of the LnI-V graph. The Norde's function also emphasizes on the entire forward bias area. As a result, it describes how the value of R_s is impacted by changes in the applied voltage.

4. CONCLUSION

In this study, the influence of R_s on the I-V plot of the *Ag/Perylene/n-Si* SBD was investigated experimentally at RT. The values of R_s have been studied via various technics, including the theorem of TE and the functions of Cheung and Norde. The R_s values of *Ag/Perylene/n-Si* SBD were found as 8.34 Ω , 4.094 Ω and 3.814 Ω and 2.125 Ω using Ohm's-Law, Cheungs'-methods and Norde-function, respectively. It was noted that some inconsistencies were attributed to the limitations of the method, specifically due to the range of voltage used. The R_{sh} value was found from I-V data in the reverse voltage region using Ohm's law as 18.45 k Ω at -5 V. R_{sh} has a high value it appropriate for an electronic device. The RR was determined from I-V data as~10³ at V= ±5 V. This indicates that the *Ag/Perylene/n-Si* SBD has a very high RR, good ohmic behavior and is also as a good diode. As a result, the basic diode parameters such as I₀, n, Φ_{B_0} and Rs values were obtained by using TE, Norde and Cheungs methods. When these results were compared, it is clear that all these parameters are depended on bias voltage or the studied voltage range.

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CONFLICT OF INTEREST

The writer states that there is no competing interest or affiliation.

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