

# Modeling and Control of a Three Level DCI in a Grid Connection Photovoltaic/Battery Storage System

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**Abstract-** This paper aimed to evaluate the use of a photovoltaic-battery storage system to supply electric power in the distribution grid through a multilevel inverter. A DC-DC converter is used to interface a photovoltaic (PV) generator and a lead-acid battery bank to a common DC-link. Maximum Power Point Tracker (MPPT) that allows extraction of maximum available power from the photovoltaic (PV) panels has been included. This MPPT is based on Perturb and observe method. A three level diode clamped inverter is connected to the grid by a traditional three phase transformer. To generate proper multilevel voltage waveforms, a fast and simplified space vector modulation is adopted. The proposed control scheme ensures the injection of a reference power in the distribution grid and controls the reactive power with fast dynamic response. The validation of the proposed system is proved by simulation of the whole system using MATLAB-Simulink software.

**Keywords-** Photovoltaic generator, MPPT, Battery bank, three level diode clamped inverter, space vector modulation, and grid utility

## 1. Introduction

With the increasing concern about global environmental protection, the need to produce pollution-free natural energy such as solar energy has received great interest as an alternative source of energy for the future since solar energy is clean, pollution-free and inexhaustible. In an effort to use the solar energy effectively, a great deal of research has been done on the grid-connected photovoltaic generation systems. In PV systems connected to the grid, the inverter that converts the output direct current (DC) of the solar modules to the alternate current (AC) is receiving increased interest in order to generate power to utility. Generally, the grid connected PV systems extract maximum power from the PV arrays. The maximum power point tracking (MPPT) technique is usually associated with a DC-DC converter. The DC-AC converter injects sinusoidal current into the grid, controlling the power factor [1-4]. In order to inject power on

demand, certain energy storage devices must be added into the system. These devices must store PV energy in excess of electricity demand and subsequently meet electricity demand in excess of PV energy. The conventional lead-acid battery is the most common energy storage device at the present time [5]. Another very important aspect of the systems connected to the grid is to select a proper power factor according to the grid demands: active or reactive power. The most efficient systems are those, which allow variation in the active and reactive power injected into the grid, depending on the power grid requirements [6]. In this scenario, we propose a control strategy for a photovoltaic- battery storage system connected to the grid with a three level diode clamped inverter.

The proposed control strategy is capable of controlling, not only the current injected into the grid, but also the power factor (PF). Varying the PF, within a certain range, the injected reactive power can be dynamically changed and controlled. The paper is organized as follow: in section 2 we

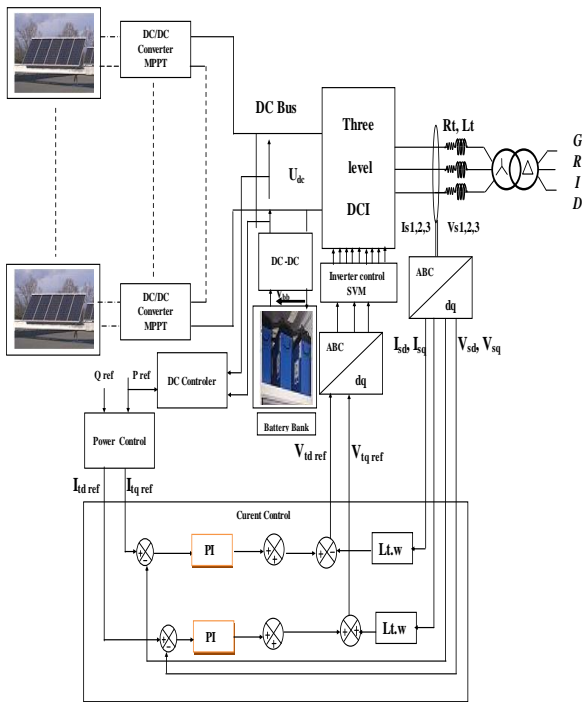
presented the the system model and control: mathematical model of the photovoltaic generator, Maximum Power Point Tracker, model of battery bank, model and control of the three level diode clamped inverter, control of the global system. Then, in section 3 we presented the simulation results and we terminated by a conclusion of this study in section 4.

**2. System Model and Control**

Fig. 1 shows the configuration of the grid-connected PV battery storage system, which consists of PV panels, DC/DC converters for MPPT, a battery bank connected to the DC bus by a bidirectional DC-DC converter and the three level diode clamped inverter connected to the grid through a traditional three phase transformer.

The control structure of the grid-connected PV – battery storage system is composed of three structure control:

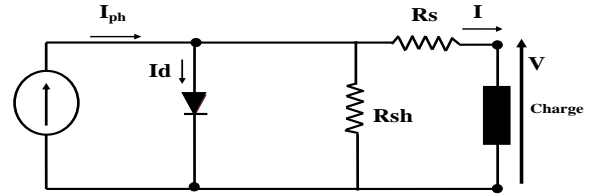
1. The MPPT Control, which the main property is to extract the Maximum power from the PV generator.
2. The inverter control, which have the main goal:
  - control the active injected into the grid;
  - control and regulate the reactive power;
  - ensure high quality of the injected power;
  - grid synchronization.
3. The DC bus control witch has the goal to regulate the DC bus and to control the charge and discharge of battery storage.



**Fig. 1.** Bloc diagram of the current control.

**2.1. Model of PV Generator**

The PV generator consists of electrically connected PV modules and it is modeled by physical oriented equivalent circuits, including one or more diode. The single diode equivalent circuit as shown in Fig. 2 is the most commonly used model for large PV generators [7].



**Fig. 2.** Photovoltaic cell equivalent circuit.

Starting from the widely known photovoltaic cell electrical equivalent circuit, an equivalent model for a more powerful PVG made of an (Ns x Np) array of PV cells, is established [4]:

The mathematical model witch relates the output current to the output voltage is given by the following expression:

$$I = I_{ph} - I_s \left[ \left( \exp \frac{V + I.R_s}{m.K.T} \right) - 1 \right] - \frac{V + I.R_s}{R_{sh}} \tag{1}$$

Where:

**I<sub>ph</sub>**: The photo-current, **I<sub>s</sub>**: the saturation current of diode, **m**: ideality factor, **R<sub>s</sub>** and **R<sub>sh</sub>**: series and parallel resistance, **T**: junction temperature, **K**: Boltzmann constant, **q**: electron charge.

For a PV module with Ns series connected cells and Np parallel connected cells, the current-voltage characteristic is given by:

$$I = N_p \cdot I_{ph} - N_p \cdot I_s \left[ \exp \left( \left( \frac{1}{m.K.T} \right) \cdot \left( \frac{V}{N_s} + \frac{R_s \cdot I}{N_p} \right) \right) - 1 \right] - \frac{N_p}{R_{sh}} \cdot \left( \frac{V}{N_s} + \frac{R_s \cdot I}{N_p} \right) \tag{2}$$

**2.2. Maximum Power Point Tracking**

The PV array must operate electrically at a certain voltage which corresponds to the maximum power point under the given operating conditions. To do this, a maximum power point tracking (MPPT) technique should be applied. Various MPPT techniques like look-up table methods, perturbation and observation (P & O) methods and computational methods have been proposed in the literature. The perturb and observe(P&O), as the name itself states that the algorithm is based on the observation of the array output power and on the perturbation (increment or decrement) of the power based on increments of the array voltage or current. The algorithm continuously increments or decrements the reference current or voltage based on the value of the previous power sample. The P&O is the simplest method which senses the PV array voltage and the cost of implementation is less and hence easy to implement [8].

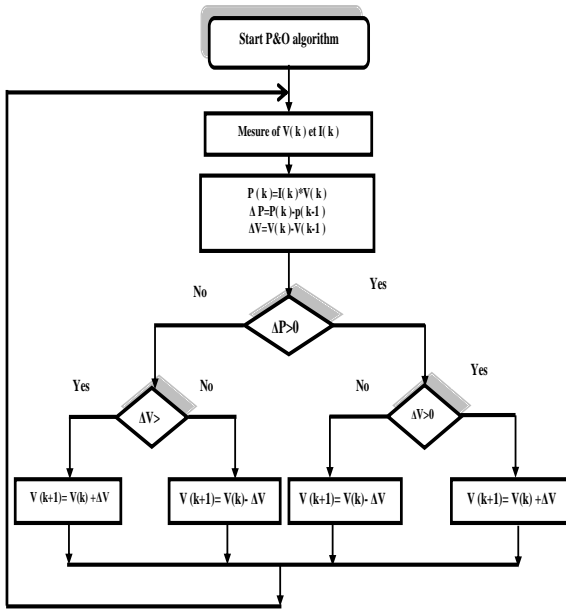


Fig. 3. Flowchart of P&O algorithm.

2.3. Battery Bank Model

Lead acid batteries are used to guarantee several hours to a few days of energy storage. The model representation of the lead-acid battery is shown in Fig. 4. The capacity of the battery is determined by integrating the main reaction current  $I_{MR}$ . To consider the increased gassing losses when charging the battery at high voltage and temperature, here represented by the loss-current  $I_{gas}$ , constitutes a significant improvement over alternative battery models for the simulation of hybrid energy systems. The state-of-charge can be calculated by referring the actual capacity to the rated capacity of the battery, as expressed by equation 2 [9-10].

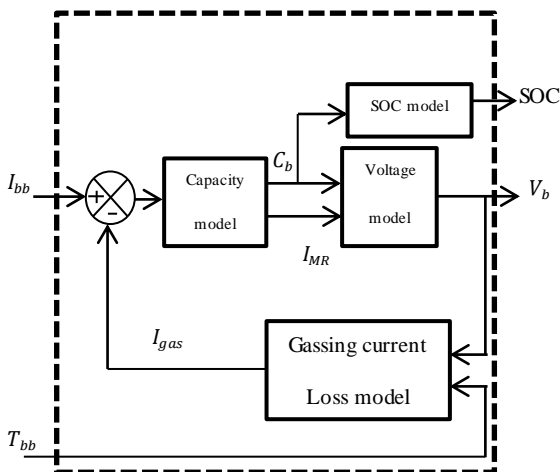


Fig. 4. General structure of battery model.

The main reaction current of the battery bank can be expressed as:

$$I_{MR}(t) = I_{bb}(t) - I_{gas}(t) \tag{3}$$

Where:

$I_{MR}$  : Main battery reaction current (A)

$I_{bb}$  : External battery current (A)

$I_{gas}$  : Battery gassing current (A)

The capacity model of the battery does not limit the charge or discharge current. Operation of the system with excessive charge or discharge currents has to be prevented by the selection of appropriately sized components and the implementation of a suitable control strategy. The actual battery capacity can be determined as:

$$C_b(t) = \int_{t=0}^t I_{MR}(t)dt + C_{b,i} \tag{4}$$

Where:

$C_b$  : Actual battery capacity (Ah)

$C_{b,i}$  : Initial battery capacity (Ah)

The state-of-charge can be calculated by referring the actual capacity to the rated capacity of the battery:

$$SOC(t) = \frac{C_b(t)}{C_{10}} \times 100\% \tag{5}$$

The presented voltage model of the battery is based on the ‘Expanded Kinetic Battery Model’, which has been presented in [9].

The voltage model considers that the battery terminal voltage depends on the following factors:

- Battery state-of-charge;
- Internal battery resistance;
- Magnitude and direction of battery current

For all calculations shown, different model parameters represent the characteristic voltage behavior of lead-acid batteries when charging or discharging. The internal battery voltage is calculated as:

a) Charging ( $I_{bb} < 0$ )

$$E_b(t) = E_{0,c} + A_c \cdot X(t) + \frac{C_c \cdot X(t)}{(D_c - X(t))^{EFC}} \tag{6}$$

b) Discharging ( $I_{bb} > 0$ )

$$E_b(t) = E_{0,d} + A_d \cdot X(t) + \frac{C_d \cdot X(t)}{(D_d - X(t))^{EFD}} \tag{7}$$

Where:

$E_b$ : Internal battery voltage (V),

X: Normalized maximum charge/discharge capacity (Ah),

The normalized maximum charge/discharge capacity X is given as:

a) Charging ( $I_{bb} < 0$ )

$$X(t) = \frac{Q_{max,c}}{Q_{max}(I_{MR}(t))} C_b(t) \tag{8}$$

b) Discharging ( $I_{bb} > 0$ )

$$X(t) = \frac{Q_{max,d} \cdot (Q_{max,d} - C_b(t))}{Q_{max} \cdot I_{MR}(t)} \tag{9}$$

The maximum capacity  $Q_{max}$  in dependence of the main reaction current of the battery is expressed by a third order polynomial equation, where the parameters have to be determined by empirical curve fitting from measured data :

a) Charging ( $I_{bb} < 0$ )

$$Q_{max}(I_{MR}(t)) = C_1 \cdot I_{MR}(t)^3 + C_2 I_{MR}(t)^2 + C_3 I_{MR}(t) + C_4 \tag{10}$$

b) Discharging ( $I_{bb} > 0$ )

$$Q_{max}(I_{MR}(t)) = D_1 \cdot I_{MR}(t)^3 + D_2 I_{MR}(t)^2 + D_3 I_{MR}(t) + D_4 \tag{11}$$

Therefore, the battery terminal voltage  $V_b$  can be calculated as:

a) Charging ( $I_{bb} < 0$ )

$$V_b(t) = E_b(t) - R_{0,c} I_{MR}(t) \tag{12}$$

b) Discharging ( $I_{bb} > 0$ )

$$V_b(t) = E_b(t) - R_{0,d} I_{MR}(t) \tag{13}$$

The voltage of a string of batteries is given by multiplying the battery voltage with the number of 12 Volt batteries in series:

$$V_{bb}(t) = B_s \cdot V_b(t) \tag{14}$$

Where:

$V_{bb}$ : Voltage of battery bank (V),

$B_s$ : Number of 12 V batteries in series,

#### 2.4. Model and Control of the Three Level DCI

Multilevel converter gives massive advantages compared with conventional and very well-known two level converters like; high power quality waveforms, low switching losses, high voltage capability, low electromagnetic compatibility (EMC) etc. At the present time, the majority of research and development effort seems to concentrate on the development of three classes of inverters: the diode-clamped multilevel inverter, the multilevel inverter with cascaded single-phase H-bridge inverters and the multilevel inverter known as the flying capacitor inverter or some-times as the imbricate cells multilevel inverter [11-13].

##### 2.4.1. Model of the three level DCI

A three-phase three-level diode-clamped inverter is shown in Fig. 5 [14]. Each leg is composed of two upper and lower switches with anti-parallel diodes. Two series dc link capacitors split the dc bus voltage in half and six clamping diodes confine the voltages across the switch within the voltages of the capacitors. The necessary conditions for the

switching states are that the dc link capacitors should not be shorted and the output current should be continuous.

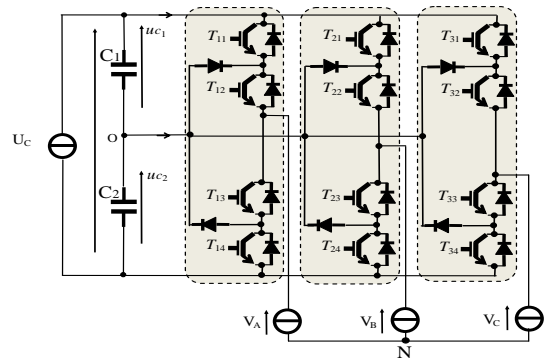


Fig. 5. Three level diode clamped inverter (DCI).

##### 2.4.1.1. Connection functions

For each leg of the inverter, we define three connection functions, each one is associated to one of the three states of the leg:

$$\begin{cases} F_{c1j} = F_{1j} \cdot F_{2j} \\ F_{c2j} = F_{2j} \cdot F_{3j} \\ F_{c3j} = F_{3j} \cdot F_{4j} \end{cases} \tag{15}$$

Table 1. The states of one leg of the 3 level DCI.

$F_{k1}$	$F_{k2}$	$F_{k3}$	$F_{k4}$	$V_k$	Etat
0	0	1	1	$-U_c/2$	N
0	1	1	0	0	O
1	1	0	0	$U_c/2$	P
1	0	0	1	unknown	-

As indicated in table 1, each leg of the inverter can have three possible switching states P,O N.

##### 2.4.1.2. Output voltages

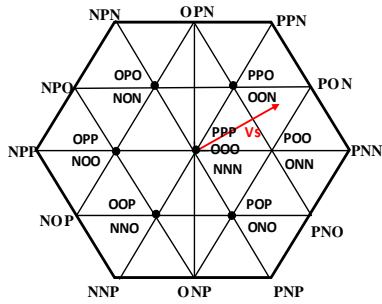
The output voltages of a three level diode clamped inverter are expressed as follows:

$$\begin{bmatrix} V_{10} \\ V_{20} \\ V_{30} \end{bmatrix} = \begin{bmatrix} F_{c11} & F_{c21} & F_{c31} \\ F_{c12} & F_{c22} & F_{c32} \\ F_{c13} & F_{c23} & F_{c33} \end{bmatrix} \cdot \begin{bmatrix} \frac{U_c}{2} \\ 0 \\ -\frac{U_c}{2} \end{bmatrix} \tag{16}$$

From the output voltages  $V_{10}$ ,  $V_{20}$  et  $V_{30}$  , we define the output voltage vector given by :

$$V_s = V_{10} e^{j0} + V_{20} e^{-j2\pi/3} + V_{30} e^{j2\pi/3} \tag{17}$$

$$V_s = V_d + j V_q \tag{18}$$

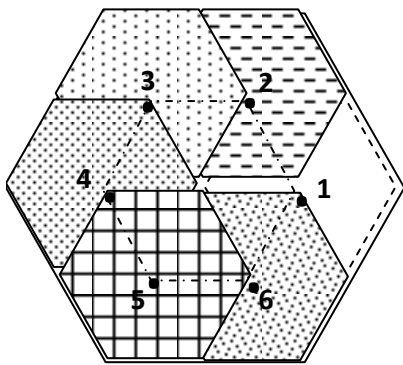


**Fig. 6.** Three level diode clamped inverter space vector diagram.

Fig. 6 shows the space vector diagram of the three level inverter. The output voltage space vector is identified by combination of switching states P,O,N of the three legs. Since three kinds of switching states exist in each leg, three level inverter has  $3^3 = 27$  switching states.

2.4.2. Simplified spave vector pulse with modulation

In SVPWM method the output voltage is approximated by using the nearest three output vectors that the nodes of the triangle containing the reference vector changes from one region to another, it may induce an output vector abrupt change. In addition we need to calculate the switching sequences and switching time of the states at every change of the reference voltage location. In this paper, a new method is proposed in which the three level inverter is decomposed into six space vector diagrams of two level inverters (Fig. 7). This modification can reduce considerably the computational time and reduce the algorithm complexity [14].



**Fig. 7.** Decomposition of the three level diode clamped inverter space vector diagram into six space vector diagrams of two level inverters.

2.4.2.1. Correction of reference voltage vector

Having the location of a given reference voltage vector, one hexagon is selected among the six small hexagons that contain the three level space vector diagram. Each hexagon is identified by a number s defined as given by:

$$s = \begin{cases} 1 & \text{si } \frac{-\pi}{3} \leq \theta \leq \frac{\pi}{3} \\ 2 & \text{si } \frac{\pi}{3} \leq \theta \leq \frac{\pi}{2} \\ 3 & \text{si } \frac{\pi}{2} \leq \theta \leq \frac{5\pi}{6} \\ 4 & \text{si } \frac{5\pi}{6} \leq \theta \leq \frac{7\pi}{6} \\ 5 & \text{si } \frac{7\pi}{6} \leq \theta \leq \frac{3\pi}{2} \\ 6 & \text{si } \frac{3\pi}{2} \leq \theta \leq \frac{11\pi}{6} \end{cases} \quad (19)$$

After selection of one hexagon, we make a translation of the reference vector  $V_s^*$  towards the center of this hexagon

**Table 2.** Correction of reference voltage vector.

Hexagon	$V_d^{*s}$	$V_q^{*s}$
1	$V_d^* - 1/2$	$V_q^*$
2	$V_d^* - 1/4$	$V_q^* - \sqrt{3}/4$
3	$V_d^* + 1/4$	$V_q^* - \sqrt{3}/4$
4	$V_d^* + 1/2$	$V_q^*$
5	$V_d^* + 1/4$	$V_q^* + \sqrt{3}/4$
6	$V_d^* - 1/4$	$V_q^* + \sqrt{3}/4$

2.4.2.2. Determination of dwelling times

Ones the corrected reference voltage  $V_s^{*s}$  and the corresponding hexagon are determined, we can apply the conventional two level space vector PWM method to calculate the dwelling times, the only difference between the two level SVPWM and the three level SVPWM is the factor 2 appearing at the first two equations as shown in this equation:

$$\begin{cases} T_1 = 2 * \left[ \frac{|V_s^{*s}| \cdot T_s \cdot \sin(\frac{\pi}{3} - \alpha)}{\sin(\frac{\pi}{3})} \right] \\ T_2 = 2 * \left[ \frac{|V_s^{*s}| \cdot T_s \cdot \sin(\alpha)}{\sin(\frac{\pi}{3})} \right] \\ T_3 = T_s - T_1 - T_2 \end{cases} \quad (20)$$

2.4.2.3. Conversion and sequence of the switching states

The reference voltage vector  $V_s^{*s}$  is approximated using the nearest three states which are nodes of the triangle containing the vector identified as X, Y and Z. the optimum sequence of these states is selected so as to minimize the total number of switching transitions. It's well known that these sequences should be reversed in the next switching interval for minimum harmonic impact as given in [14].

2.5. Control System Structure

The control structure is composed of three main blocs: the DC link regulation bloc, the power control bloc and the current control bloc as shown in Fig. 8 [15, 16].

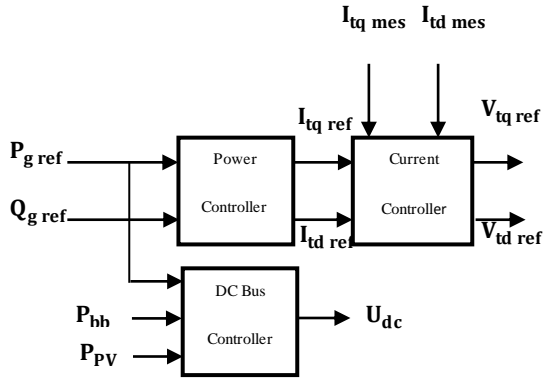


Fig. 8. Bloc diagram of the control structure.

2.5.1. DC bus control

The DC voltage control regulates the DC bus. Fig. 9 shows the bloc diagram of DC voltage control.

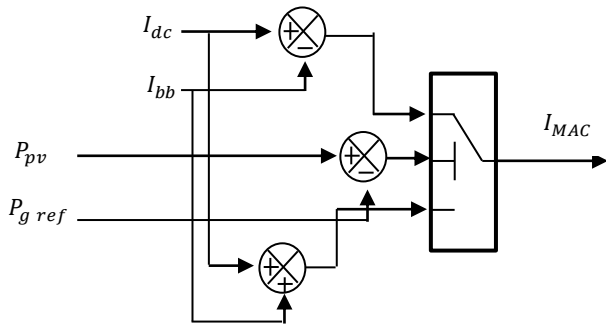


Fig. 9. Bloc diagram of the DC bus control.

Where  $I_{MAC}$  is the current in DC bus capacitor and  $I_{bb}$  is the battery bank current.

2.5.2. Power control

The active and reactive power ( $P_g, Q_g$ ) can be both expressed by using Park components of supply voltage ( $V_{td}, V_{tq}$ ) and line current ( $I_{td}, I_{tq}$ ) as follows :

$$\begin{cases} P_g = V_{td} \cdot I_{td} + V_{tq} \cdot I_{tq} \\ Q_g = V_{td} \cdot I_{tq} - V_{tq} \cdot I_{td} \end{cases} \quad (21)$$

Reference currents ( $I_{td\ ref}, I_{tq\ ref}$ ) which allows setting the desired reference active and reactive powers ( $P_{g\ ref}, Q_{g\ ref}$ ), as follows:

$$\begin{cases} I_{td\ ref} = \frac{P_{g\ ref} \cdot \hat{v}_{td} - Q_{g\ ref} \cdot \hat{v}_{tq}}{\hat{v}_{td}^2 + \hat{v}_{tq}^2} \\ I_{tq\ ref} = \frac{P_{g\ ref} \cdot \hat{v}_{tq} + Q_{g\ ref} \cdot \hat{v}_{td}}{\hat{v}_{td}^2 + \hat{v}_{tq}^2} \end{cases} \quad (22)$$

The unity power factor is obtained simply by setting the reactive power reference null. We can also generate or absorb ( $Q_{g\ ref} < 0$  or  $Q_{g\ ref} > 0$ ).

2.5.3. Current control

The vector current control in Park reference frame is carried out by using the synchronized reference with the grid voltage. The electric equations of the filter ( $R_t, L_t$ ) connected to the grid are given below:

$$\begin{cases} V_{td} = R_t I_{td} + L_t \frac{dI_{td}}{dt} - \omega_s L_t I_{tq} + V_{gd} \\ V_{tq} = R_t I_{tq} + L_t \frac{dI_{tq}}{dt} + \omega_s L_t I_{td} + V_{gq} \end{cases} \quad (23)$$

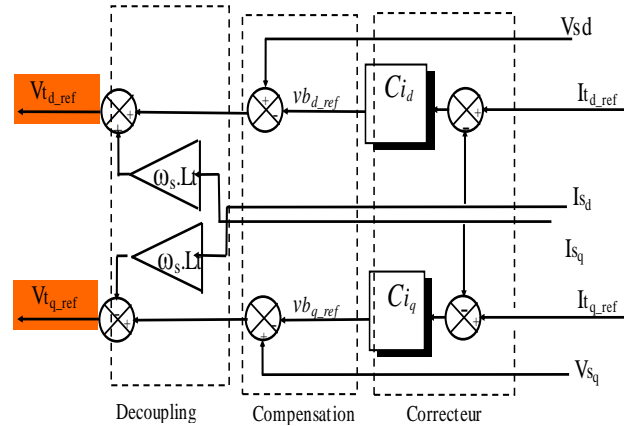


Fig. 10. Bloc diagram of the current control.

3. Result Simulation

In this section, the photovoltaic grid connexion system is simulated using SIMULINK-MATLAB. We simulate the system for two cases:

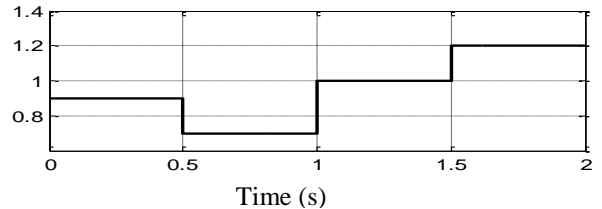


Fig. 11. Irradiance profile (sun).

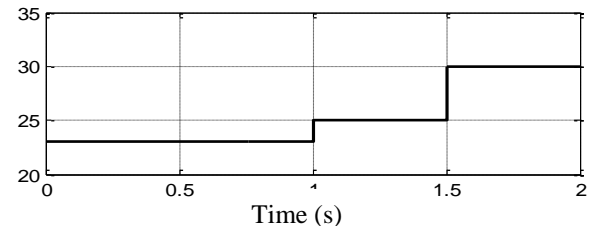


Fig. 12. Temperature (°C).

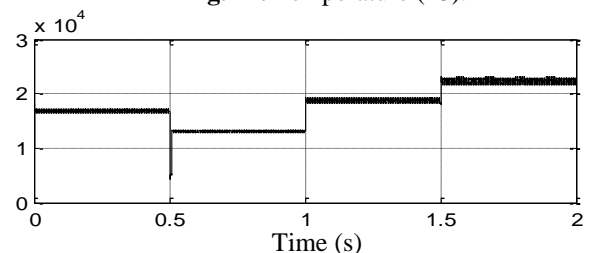


Fig. 13. Temperature (°C).

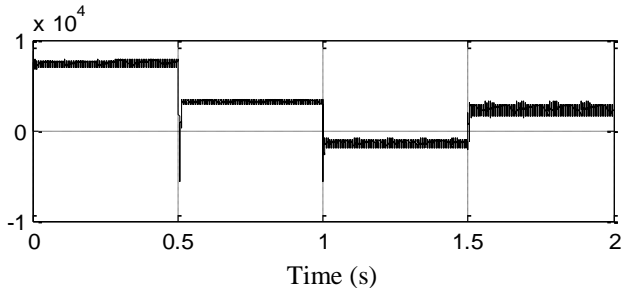


Fig. 14. Battery bank power (W).

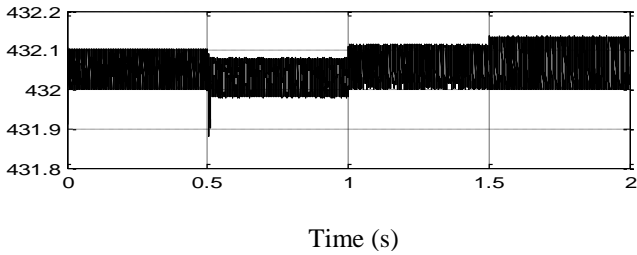


Fig. 15. DC bus voltage (V).

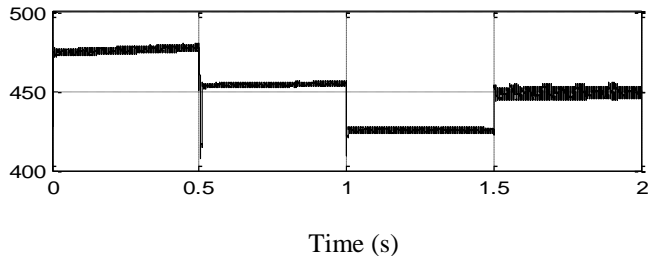


Fig. 16. DC bus voltage (V).

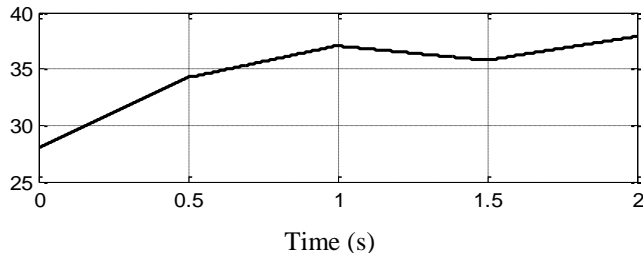


Fig. 17. State of Charge.

- case 1 : step change of  $P_g$  ref from 10 kw to 20 kw at 1s and  $Q_g$  ref = 0

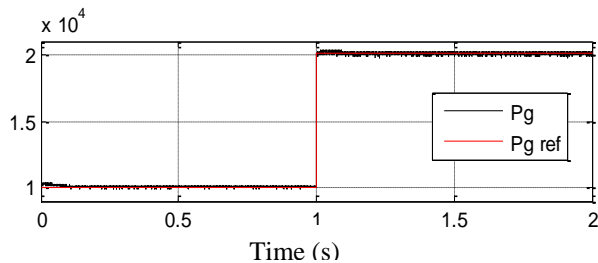


Fig. 18. Grid active power (W).

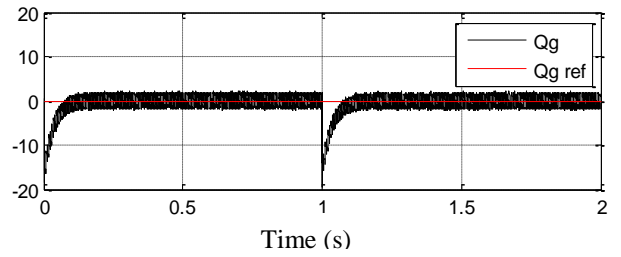


Fig. 19. Grid reactive power (var).

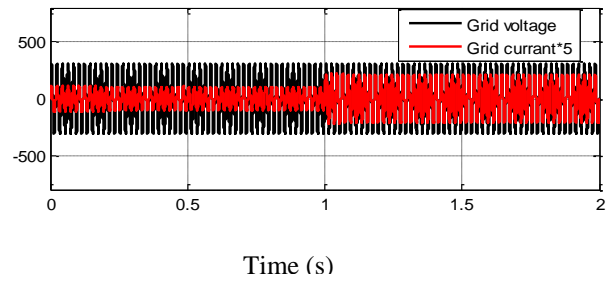


Fig. 20. Grid voltage (V) and current (A) of phase 1.

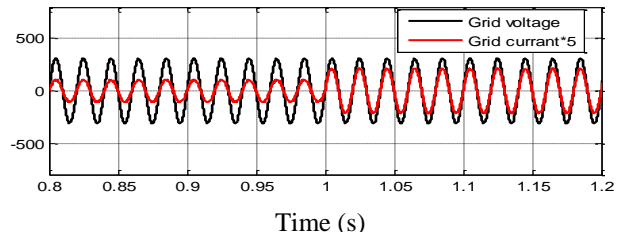


Fig. 21. Zoom of grid voltage (V) and current (A) of phase 1.

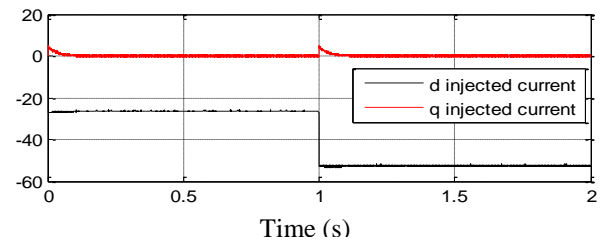


Fig. 22. d,q components of injected current (A).

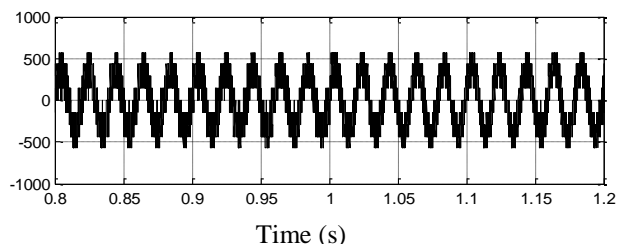


Fig. 23. Output 3 level DCI voltage phase 1 (V).

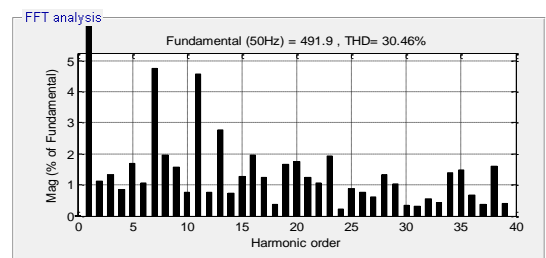


Fig. 24. THD of Output 3 level DCI voltage phase 1 (V).



- case 2 : step change of  $P_g$  ref from 10 kw to 20 kw at 1s step change of  $Q_g$  ref from -5 kw to 5 kw at 1s

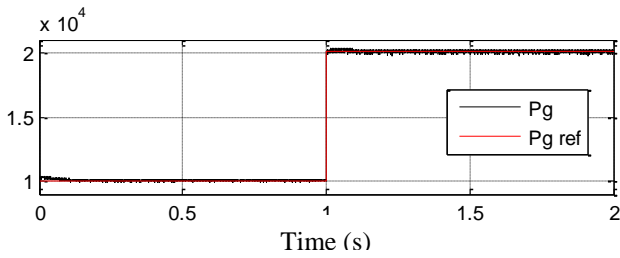


Fig. 25. Grid active power (W).

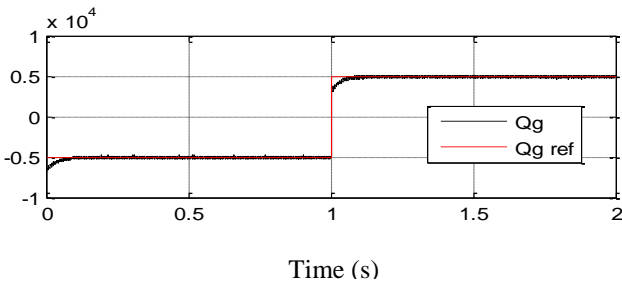


Fig. 26. Grid reactive power (var).

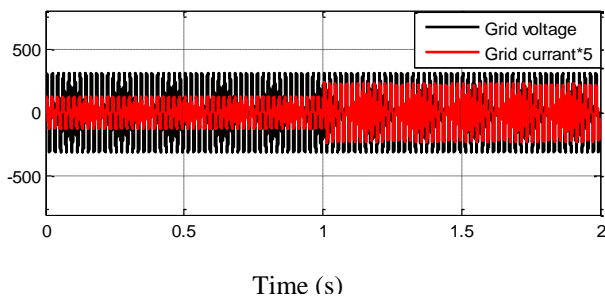


Fig. 27. Grid voltage (V) and current (A) of phase 1.

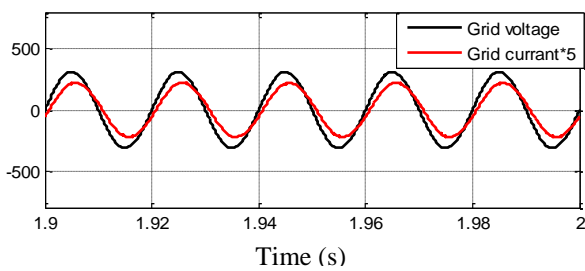


Fig. 28. Zoom of grid voltage (V) and current (A) of phase 1.

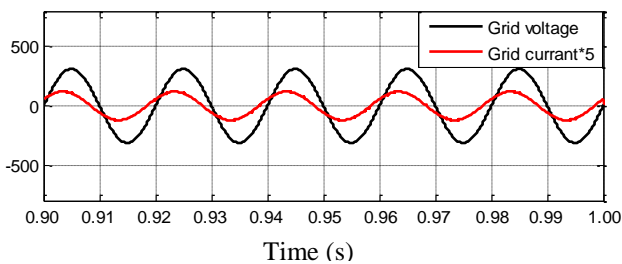


Fig. 29. Zoom of grid voltage (V) and current (A) of phase 1.

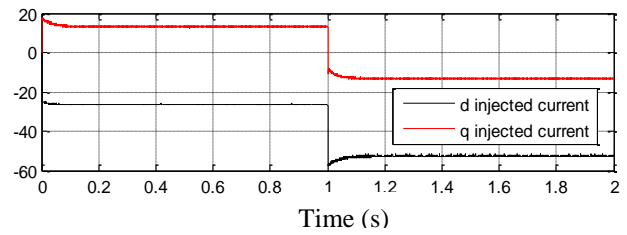


Fig. 30. d,q components of injected current (A).

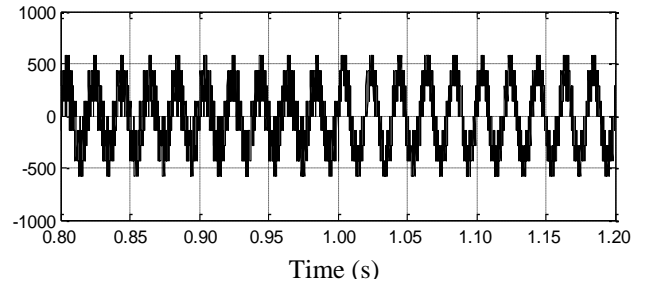


Fig. 31. Output 3 level DCI voltage phase 1 (V).

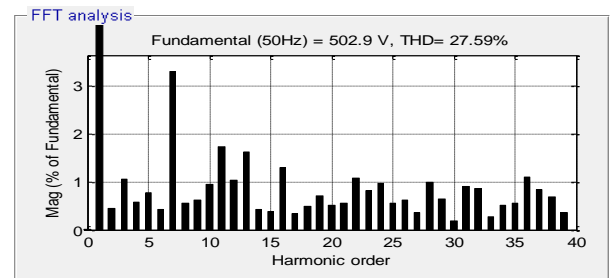


Fig. 32. THD of Output 3 level DCI voltage phase 1 (V).

These simulations improve the validity of the proposed control strategy for the grid PV/battery storage system. The results show that it is possible to inject to the grid a fixed power whatever solar irradiance and temperature condition. When the photovoltaic generator power is more than the reference power to inject into the grid, the control system ensures the multilevel inverter to inject to the grid the reference active power, the surplus energy is stocked in the battery bank and when it is less than the reference power, the battery bank compensates the difference power.

In the first case Fig. 20 and Fig. 21 show that the grid current and the grid voltage are in phase. Also, Fig. 18 and Fig. 19 show that we have injected into the grid the reference power with a unit power factor, in Fig. 23 the output voltage of the three level inverter changes when the reference power changes because the reference voltage given by the control system are changed. In the second case, the grid and current voltage are not in phase (Fig. 28, Fig. 29) because we have changed in reference reactive power (Fig. 26). We note also that the DC bus voltage remains fixed in the two cases by the control system (Fig. 15). The three level DCI generate output voltages in three levels and gives a good power quality into the grid.



**4. Conclusion**

This paper proposed the study and the control of photovoltaic-Battery storage grid connected system, the use of a three level DCI with his simplified space vector modulation as a grid interface gives a good results in term of THD and power quality, also, the aim was in this work to inject to the grid a fixed power whatever solar irradiance and temperature condition. The results obtained from this performance analysis confirm that the control strategy adopted achieves the specified performance objectives.

**Appendix**

Photovoltaic array			
<b>P<sub>max</sub></b>	150	W	Maximal power
<b>V<sub>op</sub></b>	34.5	V	Optimal voltage
<b>I<sub>op</sub></b>	4.35	A	Optimal current
<b>V<sub>oc</sub></b>	43.5	V	open circuit voltage
<b>I<sub>cc</sub></b>	4.75	A	Short circuit current
<b>N<sub>s</sub></b>	18	1	Number of series arrays
<b>N<sub>p</sub></b>	10	10	Number of parallel arrays
DC Bus			
<b>U<sub>dc</sub></b>	432	V	DC bus voltage
Filter			
<b>R<sub>t</sub></b>	3	Ω	Filter resistance
<b>L<sub>t</sub></b>	0,01	H	Filter inductance
Grid			
<b>V<sub>s</sub></b>	380	V	voltage
<b>f</b>	50	Hz	frequency
Battery bank			
<b>C<sub>10</sub></b>	118	Ah	Battery capacity at 10-hour discharge rate
<b>C<sub>V</sub></b>	8	V <sup>-1</sup>	Voltage coefficient
<b>C<sub>T</sub></b>	0.05	K <sup>-1</sup>	Temperature coefficient
<b>I<sub>G0</sub></b>	0.035	A	Normalised gassing current
<b>Q<sub>max,C</sub></b>	120	Ah	Maximum charge capacity
<b>Q<sub>max,D</sub></b>	130	Ah	Maximum discharge capacity
<b>R<sub>I,C</sub></b>	75	mΩ	Internal resistance when charging the battery
<b>R<sub>I,D</sub></b>	38	mΩ	Internal resistance when discharging the battery
<b>E<sub>0,C</sub></b>	11.6	V	Limiting internal battery voltage for zero current and fully discharged battery after the initial transient
<b>A<sub>C</sub></b>	0.01	-	Parameter reflecting the initial linear variation of the internal battery voltage with increasing state-of-charge
<b>C<sub>C</sub></b>	0.012	-	Parameter reflecting increasing voltage when battery is progressively charged
<b>D<sub>C</sub></b>	130	-	Parameter reflecting sharp increase of voltage when

			battery is charged to a high SOC
<b>EF<sub>C</sub></b>	0.45	-	Exponential factor introduced to achieve a closer curve fit for voltage behaviour when progressively charged
<b>E<sub>0,d</sub></b>	12.6	V	Fully charged internal battery voltage when discharging after the initial transient
<b>A<sub>D</sub></b>	-0.007	-	Parameter reflecting the initial linear variation of the internal battery voltage with decreasing state-of-charge
<b>C<sub>D</sub></b>	-0.3	-	Parameter reflecting decreasing voltage when battery is progressively discharged
<b>D<sub>D</sub></b>	165	-	Parameter reflecting sharp decrease of voltage when battery is discharged to a low SOC
<b>EF<sub>D</sub></b>	1.25	-	Exponential factor introduced to achieve a closer curve fit for voltage behaviour when progressively discharged
<b>C<sub>1</sub></b>	0.0006	h/A <sup>2</sup>	Parameter to calculate maximum charge capacity
<b>C<sub>2</sub></b>	0.0543	h/A	Parameter to calculate maximum charge capacity
<b>C<sub>3</sub></b>	2.0279	h	Parameter to calculate maximum charge capacity
<b>C<sub>4</sub></b>	140.29	Ah	Parameter to calculate maximum charge capacity
<b>D<sub>1</sub></b>	-0.0006	h/A <sup>2</sup>	Parameter to calculate maximum discharge capacity
<b>D<sub>2</sub></b>	0.0543	h/A	Parameter to calculate maximum discharge capacity
<b>D<sub>3</sub></b>	-2.0279	h	Parameter to calculate maximum discharge capacity
<b>D<sub>4</sub></b>	140.29	Ah	Parameter to calculate maximum discharge capacity
<b>B<sub>s</sub></b>	36	-	Number of series batteries

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