

The wave-front parallel processing accelerates encoding of source stream by dividing into slices and tiles. Multi-directional operation of intra-picture prediction between 0 and 34 gives additional compression performance while keeping perceptual quality at CBR and VBR [1].

3. Experimental Results

We use the reference software of Dirac (the latest version of pure Dirac is 1.0.2), H.264 (JM18.5) and HEVC (HM 11.0) respectively [9,10,11]. Akiyo, Stefan and Caesar test video clips are used with QCIF, CIF and VGA (640x400) resolutions respectively. 30f/s and 40f/s frame rates are used for performance comparison. All reference software is fully implemented in the C/C++ programming languages. These codes are executed by 64-bit Intel i5 processor, running at 2.4GHz.

Encoding configurations are as follows: The Rate Distortion Optimization (RDO) mode and loop-filter are enabled in H.264 main profile and HEVC encoder intra main of 8bits. In H.264, HEVC and Dirac, fast full search mechanisms are activated. Motion vector accuracy was specified as 1/8 in Dirac. Quality Parameter (QP) values of these codecs are 7, 28 and 32 respectively. Moreover, Dirac wavelet organization is structured by using Deslauriers-Dubuc 13/7 interpolation lifting filter as a default concept [12]. Most of these settings are typical settings for the respective codec. The simulation results are analyzed in three aspects: PSNR, SSIM (by using [13]'s environment) and encoding time at VBR.

Dirac has the best performance in objective quality assessments (PSNR, SSIM), however it has a worse Compression Ratio (CR) as shown in Figure.4 and Table 1. HEVC provides the best compression ratio. However, if the compressed file size is not very critical, Dirac may be a good no-cost selection.

As investigated in Figure.5, Dirac's simplicity significantly outperforms HEVC and H.264 regarding computational in encoding speed.



Figure 3. Test video clips (Upper-left Akiyo (50 frames @30Hz), upper-right Stefan (90 frames @30Hz), lower Caesar (101 frames @40Hz)) [14]

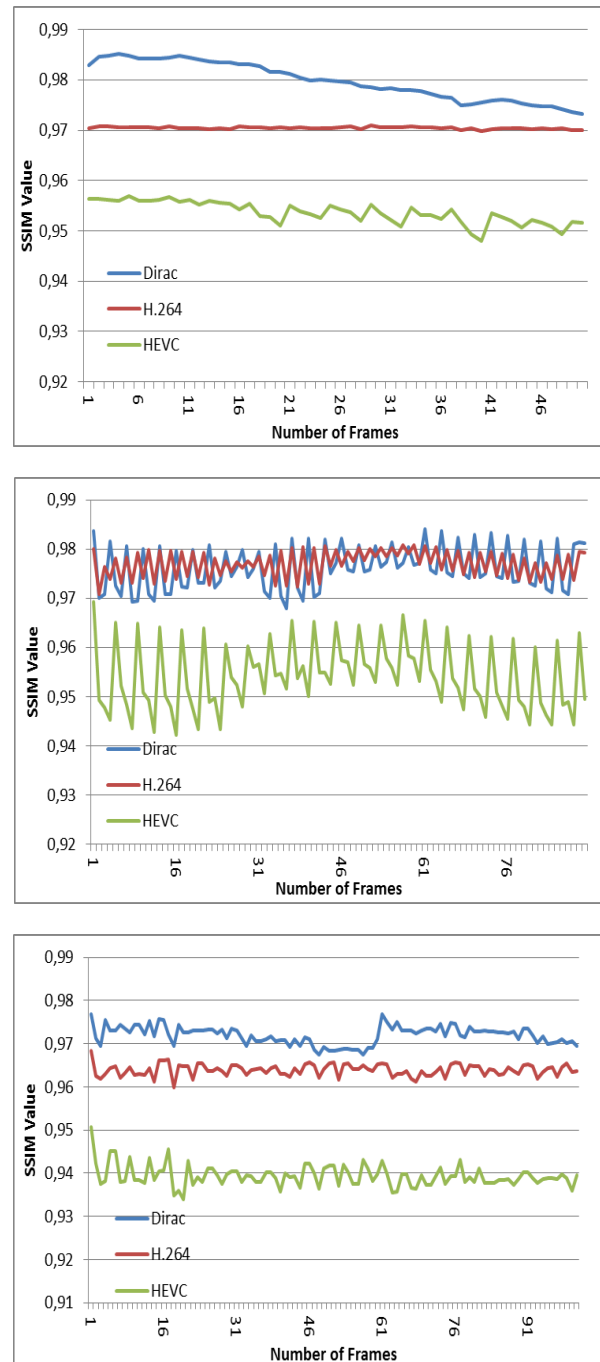


Figure 4. SSIM performance comparison at VBR (Upper Akiyo, center Stefan, lower Caesar)

Dirac entropy coding used in this paper is achieved by replacing the original Dirac arithmetic coder with an accurately configured M-coder [15]. The new scheme is three times faster for high bit rates. Furthermore, H.264 and HEVC have more complex entropy coding approaches, consisting Context Adaptive Binary Arithmetic Coding (CABAC) and header formatting layers. Moreover, the reference software of these codecs is non-optimized. However, the speed rankings among the three codecs do not change, due to the fact that the optimized performance can only be up to three times faster than the non-optimized performance [1,2,3].

Table 1. Overall performance comparison for three codecs

Source Stream	Video Codecs	SSIM (Mean)	PSNR (Mean-dB)	Encoded File Size (KB)	CR (%)
Akiyo	Dirac	0.979	40.6	127	98.85
	H.264	0.9	38.74	7	99.93
	HEVC	0.953	36.22	4	99.96
Stefan	Dirac	0.976	35.71	590	95.58
	H.264	0.977	35.85	341	97.44
	HEVC	0.95	31.7	110	99.17
Caesar	Dirac	0.971	39.4	458	98.79
	H.264	0.963	38	165	99.56
	HEVC	0.94	34.75	56	99.85

4. Conclusions

To enhance previous results on performance comparison between H.264, Dirac and HEVC video codecs at CBR [16], a VBR performance comparison is presented in this work. In addition to PSNR and SSIM, encoding time is included in our analyses.

Using typical quality parameters and the three test cases, the following can be observed: Dirac outperformed H.264 and HEVC in PSNR and SSIM. However, HEVC outperformed H.264 and significantly exceeded Dirac for encoded file size. Dirac had significantly shorter encoding times with respect to H.264 and HEVC. Among the three codecs compared, Dirac is the only open source, royalty free codec.

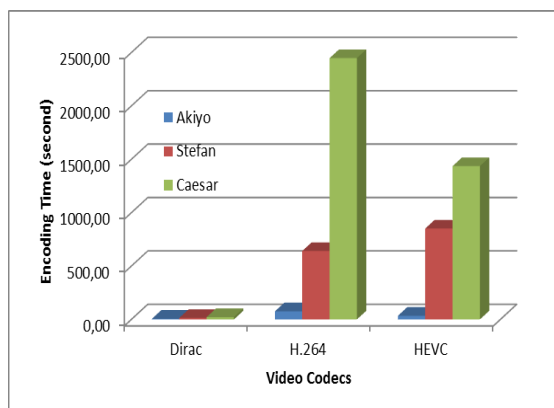


Figure 5. Computational performance comparison

5. References

- [1] G.J.Sullivan, J.R.Ohm, W.J.Han, T.Wiegand, "Overview of the High Efficiency Video Coding (HEVC) Standard", IEEE Transactions on Circuits and Systems for Video Technology, vol.22, no.12, pp. 1649-1668, 2012.
- [2] T.Borer and T.Davies, "Dirac Video Compression Using Open Technology", BBC EBU Technical Review, July 2005.
- [3] B. Bross, W.J. Han, G. J. Sullivan, J.-R. Ohm, and T. Wiegand, "High efficiency video coding (HEVC) text specification draft 8," ITU-T/ISO/IEC Joint Collaborative Team on Video Coding (JCT-VC) document JCTVC-J1003, July 2012.
- [4] J.B.Lee and H.Kalva, "The VC-1 and H.264 Video Standards for Broadband Video Services", Springer, 2008.
- [5] I.E.G.Richardson, "H.264 and MPEG-4 Video Compression", The Robert Gordon University, UK, Wiley, 2003.
- [6] A.Ravi and K.R.Rao, "Performance Analysis and Comparison of the Dirac Video Codec with H.264/MPEG-4, Part 10, Advances in Reasoning-Based Image Processing, Springer, ISRL 29, pp. 9-34, 2012.
- [7] D.Özenli and M.Pazarci, "Performance Analysis of Dirac Video Codec in Different Motion Vector Accuracies and Wavelet Lifting Decompositions", IEEE Elmar Proceedings, pp. 63-66, 2011.
- [8] T.Davies, "The Dirac Algorithm", 2008, <http://dirac.sourceforge.net/documentation/algorithm/>
- [9] Dirac software and source code, <http://diracvideo.org/download/dirac-research>
- [10] H.264AVCJMSsoftware, <http://iphome.hhi.de/suering/tm1/>
- [11] HEVC software and source code, https://hevc.hhi.fraunhofer.de/svn/svn_HEVCSoftware/tags/HM-1.0/
- [12] Dirac video codec-A programmer's guide, http://dirac.sourceforge.net/documentation/code/programmers_guide/toc.htm.
- [13] MSU Video Quality Measurement Tool, http://compression.ru/video/quality_measure/video_measurement_tool_en.html
- [14] Video test sequences (YUV 4:2:0), <http://trace.eas.asu.edu/yuv/index.html>
- [15] H.Eeckhaut, B.Schrauwen, M.Christiaens, J.V.Campenhout, "Tuning the M-coder to improve Dirac's Entropy Coding, WSEAS Transactions on Information Science and Applications, vol. 2, pp. 1563-1571, 2005.
- [16] K.R.Rao, D.N.Kim, J.J.Hwang, "Video coding standards -AVS China, H.264/MPEG-4 PART 10, HEVC, VP6, DIRAC and VC-1", Springer, 2014.



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