

## Gazi Üniversitesi **Fen Bilimleri Dergisi** PART C: TASARIM VE TEKNOLOJİ

# Gazi University Journal of Science PART C: DESIGN AND

**TECHNOLOGY** 



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## The PIDA Controller Analysis Simulator

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## Makale Bilgisi

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### Anahtar Kelimeler

Kontrol sistemi PIDA denetleyici Simülatör

## Graphical/Tabular Abstract (Grafik Özet)

In this study; a software tool with a user-friendly interface has been designed that performs control systems simulations with different type PIDA controllers in an accurate, effective, fast and simple manner. / Bu çalışmada; farklı tipteki PIDA denetleyicilerle kontrol sistemlerinin simülasyonlarını doğru, etkili, hızlı ve basit bir şekilde gerçekleştiren, kullanıcı dostu arayüze sahip bir yazılım aracı tasarlanmıştır.

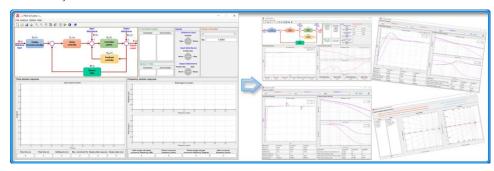


Figure A: The designed simulator / Şekil A: Tasarlanan simülatör

## Highlights (Önemli noktalar)

- An interactive simulator was designed for the PIDA controllers in the control systems. / Kontrol sistemlerinde PIDA denetleyicileri için etkileşimli bir simülatör tasarlandı.
- Performance evaluations of PID and PIDA type controllers can be easily performed with time and frequency domain analysis. / PID ve PIDA tipi kontrolörlerin performans değerlendirmeleri zaman ve frekans domeni analizi ile kolayca gerçekleştirilebilir.
- The designed simulator has capabilities such as comparative analysis of the effects of reference and disturbance inputs and comparative analysis of some performance parameters related to time/frequency response. / Tasarlanan simulator referans ve bozucu girişlerinin etkilerinin karşılaştırmalı analizi ve zaman/frekans cevabı ile ilgili bazı performans parametrelerinin karşılaştırmalı analizi gibi yeteneklere sahiptir.

Aim (Amaç): This study aims to design of software tool to analyze effects of the PIDA controllers to the system performance. / Bu çalışma, PIDA denetleyicilerinin sistem performansına olan etkilerini analiz etmek için bir yazılım aracının tasarlanmasını amaçlamaktadır.

Originality (Özgünlük): The originality of this study is the development of a new software tool (simulator) that performs comprehensive analysis for PIDA controllers. / Bu çalışmanın özgünlüğü, PIDA denetleyiciler için kapsamlı analizler gerçekleştiren yeni bir yazılım aracının (simülatör) geliştirilmesidir.

Results (Bulgular): The important features of the developed simulator are demonstrated with three case studies (maglev vehicle suspension system, automatic voltage regulator, induction motor system) performed in the work. / Geliştirilen simülatörün önemli özellikleri çalışmada gerçekleştirilen üç örnek (maglev araç süspansiyon sistemi, otomatik voltaj regülatörü, endüksiyon motor sistemi) ile ortaya konmuştur.

**Conclusion (Sonuç):** The developed interactive simulator provides effective computer support for analyzing the PIDA type controllers. / Geliştirilen etkileşimli simülatör, PIDA tipi denetleyicileri analiz etmek için etkili bilgisayar desteği sunar.



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## The PIDA Controller Analysis Simulator

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## **Abstract**

The Proportional-Integral-Derivative (PID) is a traditional controller type widely used in industrial control systems. However, sometimes they may be insufficient in controlling higher degree systems. In these cases, the Proportional-Integral-Derivative-Acceleration (PIDA) controllers can be preferred. In this study; a software tool with a user-friendly interface has been designed that performs control systems simulations with different type PIDA controllers in an accurate, effective, fast and simple manner, produced single or comparative numerical and graphical results (performance parameters, time and frequency domain responses, etc.). The features and capabilities of the designed simulator have been tested with some sample systems: maglev vehicle suspension system, automatic voltage regulator and induction motor systems. The developed simulator with ease and contents is suitable for all students, engineers, and users from this area.

## PIDA Denetleyici Analiz Simülatörü

### Makale Bilgisi

Araştırma makalesi Başvuru: 27/12/2024 Düzeltme: 19/02/2025 Kabul: 23/03/2025

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## Öz

Oransal-İntegral-Türev (PID), endüstriyel kontrol sistemlerinde yaygın olarak kullanılan geleneksel bir denetleyici türüdür. Ancak, bazen daha yüksek dereceli sistemlerin kontrolünde yetersiz kalabilmektedirler. Bu durumlarda Oransal-İntegral-Türev-İvme (PIDA) denetleyiciler tercih edilebilir. Bu çalışmada; farklı tip PIDA denetleyicilerle kontrol sistemleri simülasyonlarını doğru, etkili, hızlı ve basit bir şekilde gerçekleştiren, tek veya karşılaştırmalı sayısal ve grafiksel sonuçlar (performans parametreleri, zaman ve frekans alanı tepkileri vb.) üreten, kullanıcı dostu arayüze sahip bir yazılım aracı tasarlanmıştır. Tasarlanan simülatörün özellikleri ve yetenekleri bazı örnek sistemlerle test edilmiştir: maglev araç süspansiyon sistemi, otomatik voltaj regülatörü ve endüksiyon motor sistemleri. Geliştirilen simülatör, kullanım kolaylığı ve içeriği ile bu alandaki tüm öğrenciler, mühendisler ve kullanıcılar için uygundur.

## 1. INTRODUCTION (GİRİŞ)

The Proportional-Integral-Derivative (PID) controller is the most preferred and widely used controller type in industrial control systems due to its easy design and implementation [1-2]. However, if the degree of the controlled system is three or greater, the two zeros provided by the PID controller may be insufficient to obtain the desired response accurately. To overcome this problem and obtain the desired system response more accurately, Jung and Dorf proposed Proportional-Integral-Derivative-Acceleration (PIDA) controller and an analytical method to determine the coefficients of this controller [3-4]. The PIDA controller, also known in the literature as the PID double derivative (PIDD2 or PIDD2) controller, has a second derivative operator in addition to the PID controller, thus adding three zeros to the system. Therefore, it provides more phase-lead to the system, further improving the transient response of the system and providing extra robustness against disturbance inputs.

The PIDA controller has attracted the attention of researchers in recent years for third order systems [3-14], fourth orders systems [15-18], integral plus dead time systems [19-22], double integrator plus dead time systems [23], first order plus dead time systems [24], second order plus dead time systems [25], second order oscillatory with time delay system [26-28], second-order oscillatory model with zero plus dead time systems [29], third order plus dead time systems [30-31], different higher order systems [32], higher order time delay systems [33-36], higher order systems with integral and dead time [37], non-minimum phase second order system [38], two-input two-output nonlinear system [39],

uncertain time delay systems [40], nonlinear uncertain systems [41], interconnected time delay systems [10, 31, 42-44]. Some generalized PID type controllers have also been proposed by adding higher order derivative terms to PIDA controllers [45-49]. These studies also include the PIDA controller.

While there are many simulators created for PID, only a few simulators with limited features have been developed for PIDA. A few simulation tools were designed in the literature to understand the PIDA controllers [50-54]. A software tool for the disturbance analysis of PID with higher derivative degrees was developed by Bisták. Graphical results in the simulator also include performance measurements and noise characteristics with time domain responses [50]. In a different study the designed simulation tool provides a comparison of set-point and step disturbance responses in the higher derivative degree PID controller circuit with the first order time-delayed controlled system influenced by a noise [51]. In another work, Bisták and Huba developed online software tool to help with dissemination of PID controllers with higher derivative degree [52]. In the simulation tool developed by Ferrari and Visioli can modify the PIDA parameters and analyze the corresponding changes in system responses in time (reference input, step disturbance and noise rejection performance) and frequency domain [53]. In another study a simple interactive educational tool for teaching PID and PIDA control is presented [54].

The aim of this study is to design a comprehensive analysis simulator that includes different PIDA controllers in the literature. Existing simulators usually contain limited controller structures and also have limited features. In this study, an interactive simulator is designed for basic PID and PIDA controllers. This simulator, which includes P, PI, PD, PID, PIDA, I-PD, I-PDA, PID with prefilter and PIDA with prefilter controllers and can be used for educational and application purposes, can perform single and comparative analyses. As a result of the analysis of user-defined systems with selected controllers under different conditions (input disturbance, output disturbance etc.), many numerical and graphical results such as time and frequency responses, response parameters, system properties are obtained. Thus, suitability/efficiency/performance of the relevant controllers for the defined systems under the specified conditions can be evaluated quickly, easily and effectively.

This paper is organized as follows: In Section 2, PIDA controllers are summarized. In Section 3, the designed PIDA analysis simulator is explained and some sample applications are given. Finally, Section 4 contains conclusions.

# **2. PIDA CONTROLLERS** (PIDA DENETLEYÍCÍLER)

The classical PIDA controller (Figure 1) can be written as follows in time-domain and s-domain, respectively:

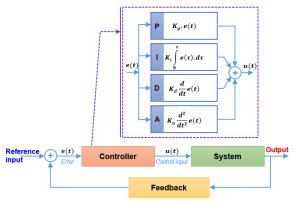
$$u(t) = K_{p} \cdot e(t) + K_{i} \int_{0}^{t} e(\tau) d\tau + K_{d} \frac{d}{dt} e(t) + K_{a} \frac{d^{2}}{dt^{2}} e(t)$$
(1)

$$U(s) = \left(K_p + \frac{\kappa_i}{s} + K_d s + K_a s^2\right) E(s) \tag{2}$$

If the derivative filter is used in the PIDA controller, it can be written as follows; where, a, b, c are controller zeros, and d, e are controller poles [3-4]:

$$U(s) = \left(K_p + \frac{K_i}{s} + \frac{K_d s}{(s+d)} + \frac{K_a s^2}{(s+d)(s+e)}\right) E(s)$$

$$= K \frac{(s+a)(s+b)(s+c)}{s(s+d)(s+e)} E(s)$$
(3)



**Figure 1.** The feedback control system and PIDA controller (Geribeslemeli kontrol sistemi ve PIDA denetleyici)

In the PIDA controller, it can be written as follows, where  $\alpha$ ,  $\beta$  and  $\gamma$  are set-point weighting parameters for proportional, derivative and acceleration parts, respectively:

$$U(s) = K_{p}[\alpha R(s) - Y(s)] + \frac{\kappa_{i}}{s} E(s) + K_{d} s[\beta R(s) - Y(s)] + K_{a} s^{2} [\gamma R(s) - Y(s)]$$
(4)

By taking the set-point weighting parameters as zero, I-PDA controller is obtained [55]:

$$U(s) = \frac{\kappa_i}{s} E(s) - \left(K_p + K_d s + K_a s^2\right) Y(s) \tag{5}$$

If the set-point weighting and derivative filter are used together in the PIDA controller, the following equation can be written:

$$U(s) = K_p[\alpha R(s) - Y(s)] + \frac{K_i}{s}E(s) + \frac{K_ds}{(s+d)}[\beta R(s) - Y(s)] + \frac{K_ds^2}{(s+d)(s+e)}[\gamma R(s) - Y(s)]$$
 (6)

# 3. DESIGNED TOOL and APPLICATIONS (TASARLANAN ARAÇ VE UYGULAMALARI)

In this study, an interactive simulator was designed using MATLAB App Designer [56] for PID and PIDA controllers with different types. This simulator includes P, PI, PD, PID, PIDA, I-PD, I-PDA, PID with prefilter and PIDA with prefilter controllers. The general block diagram of the system used for analysis is seen on the simulator main screen in Figure 2. The main screen, toolbar and main menu of designed simulator with their descriptions are given in Figure 2 and Table 1-2, respectively. On the simulator main screen (Figure 2):

- The transfer function of the controlled system can be entered as numerator and denominator coefficients.
- The transfer function of the feedback system (sensor/filter) can be entered as numerator and denominator coefficients.

- Impulse, step and ramp can be selected as reference input.
- If available, user-defined step and partial step can be set as input disturbance.
- If available, user-defined step, partial step and noise can be set as output disturbance.
- The series controller is selected and its coefficients are entered. In comparative analyses, three series controllers are selected and their coefficients are entered separately.
- As a result of single analysis, time and frequency domain responses are plotted. In addition, the parameters in the relevant domains (for time domain: rise time, peak time, settling time, overshoot percentage, steady state response, steady state error; for frequency domain: gain margin, phase margin, gain crossover frequency, phase crossover frequency) are also displayed on the screen.
- As a result of the comparative analysis, the time and frequency domain responses are plotted comparatively with the adjusted controllers. In addition, the parameters in the relevant domains are given comparatively in tables.

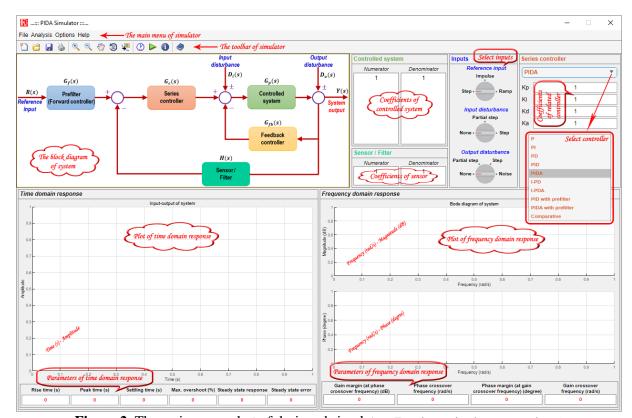


Figure 2. The main screenshot of designed simulator (Tasarlanan simülatörün ana ekranı)

Table 1. The toolbar of designed simulator (Tasarlanan simülatörün araç çubuğu )



**Table 2.** The main menu of designed simulator (Tasarlanan simülatörün ana menüsü)

| File                                    |       |                  |                                  | Analysis |            |                                                  | Options |  |                  |           | Help  |         |          |        |   |
|-----------------------------------------|-------|------------------|----------------------------------|----------|------------|--------------------------------------------------|---------|--|------------------|-----------|-------|---------|----------|--------|---|
|                                         | New   | Ctrl+N<br>Ctrl+O |                                  |          | Single     | Ctrl+S                                           |         |  | Simulation range | Time      |       |         | Contents | Ctrl+H |   |
|                                         | Save  | Cui+O            |                                  |          | Comparativ | e Ctrl+C                                         |         |  | Format           | Frequency |       |         | About    |        | 1 |
|                                         | Print | Ctrl+P           |                                  |          |            |                                                  | _       |  | Graphics         |           |       |         |          |        |   |
|                                         | Exit  |                  |                                  |          |            |                                                  |         |  | •                |           |       |         |          |        |   |
| Basic operations for simulation: new,   |       |                  | Performing single or comparative |          |            | Simulation (time/frequency interval) and display |         |  |                  | Simulato  | or co | ntent/u | ıser     |        |   |
| open, save (as data or image) and print |       |                  | simulations/analysis             |          |            | (format, graphic properties, etc.) options       |         |  |                  | guide     |       |         |          |        |   |

Case study-1: Maglev vehicle suspension system

The maglev vehicle can be modeled as a onedimensional vehicle model consisting of a primary part (chassis) and a secondary part (passenger cabin). Transfer function model of the maglev suspension system from the guideway disturbance to position of the passenger cabin is given as

$$G_p(s) = \frac{b_p b_s s^2 + (b_p k_s + b_s k_p) s + k_p k_s}{m_p m_s s^4 + [m_p b_s + m_s (b_p + b_s)] s^3 + [m_p k_s + m_s (k_p + k_s) + b_p b_s] s^2 + (b_p k_s + b_s k_p) s + k_p k_s}$$

$$(7)$$

where masses  $m_p$  and  $m_s$ , stiffnesses  $k_p$  and  $k_s$ , and dampings  $b_p$  and  $b_s$  are the model parameters of the primer and seconder parts. Using the values

for the model parameters given in [16], the following transfer function is obtained:

$$G_p(s) = \frac{9.944 \cdot 10^{10} s^2 + 6.271 \cdot 10^{12} s + 4.555 \cdot 10^{13}}{9.344 \cdot 10^8 s^4 + 3.838 \cdot 10^{10} s^3 + 1.949 \cdot 10^{12} s^2 + 6.271 \cdot 10^{12} s + 4.555 \cdot 10^{13}}$$
(8)

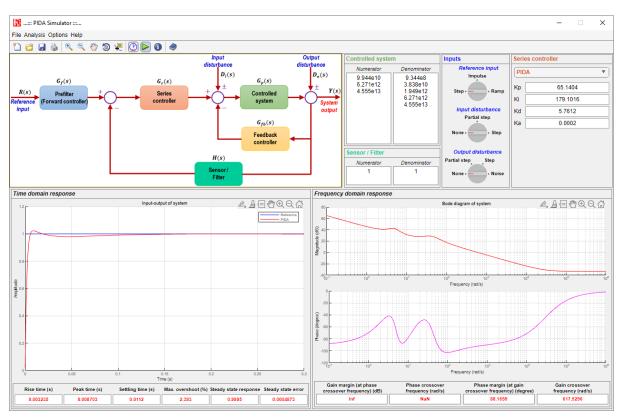
In the first simulation, the following PIDA controller given in [16] was used to control the maglev vehicle suspension system.

$$G_c(s) = 65.1404 + \frac{179.1016}{s} + 5.7612s + 0.0002s^2$$

$$H(s) = 1$$
(9)

The simulation result screen is shown in Figure 3a. Here, the time and frequency domain responses and related response parameters are included. Thus, the performance of the user-defined system with the

specified controller can be clearly seen. If the desired specifications are not satisfied in the system response, simulations can be easily repeated by changing the controller and its coefficients. In addition, the window in Figure 3b is opened with the "Detailed information" icon. Here, the closed-loop transfer function of the system is printed in different ways, the zeros and poles of the system are listed and mapped, and the root locus curve is shown.



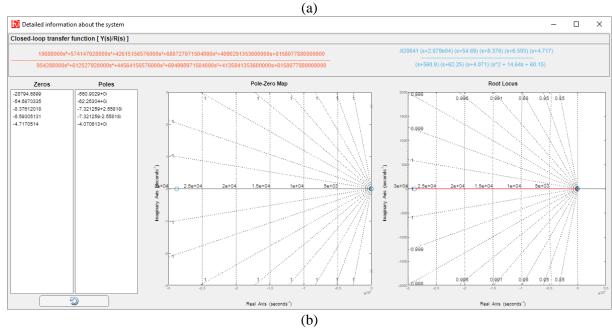


Figure 3. The screenshots for first application (Birinci uygulama için ekran görüntüleri)

Case study-2: Automatic voltage regulator system

The second application is comparative control (PID, PIDA, PIDA) of automatic voltage regulator (AVR) system. An AVR system is used to regulate the generator voltage in a power system and consists of amplifier, exciter and generator blocks connected in series on forward path and a sensor on feedback path of the control system. An error information is obtained by measuring the terminal voltage of the generator with a sensor and comparing it with the reference voltage. Any designed controller processes this error signal and produces a control input that is used to excite the series-connected amplifier, exciter, generator blocks. Each block in the AVR system is modeled with a first-order system model defined by a gain and a time constant, and using the model parameters given in [15, 57-58] for each block, the following transfer function is obtained; where  $k_a, k_e, k_g, k_s$  are the gains and  $\tau_a$ ,  $\tau_e,\,\tau_g,\,\tau_s$  are the time constants of the amplifier, exciter, generator and sensor blocks, respectively.

$$G_{p}(s) = \frac{k_{a}}{1+\tau_{a}s} \cdot \frac{k_{e}}{1+\tau_{e}s} \cdot \frac{k_{g}}{1+\tau_{g}s}$$

$$= \frac{10}{0.04s^{3}+0.54s^{2}+1.5s+1}$$

$$H(s) = \frac{k_{s}}{1+\tau_{s}s} = \frac{1}{0.01s+1}$$
(10)

In the second simulation, the performances of the PID controller given in [57] and the PIDA

controllers given in [15] and [58] were compared by using the AVR control system. Also, a step disturbance with an amplitude of 0.1 V was applied to the system output at t=3 s, and removed at t=5 s.

$$G_{c_{PID}}(s) = K_{p} + \frac{K_{i}}{s} + K_{d}s$$

$$= 0.61982 + \frac{0.41654}{s} + 0.21269s$$

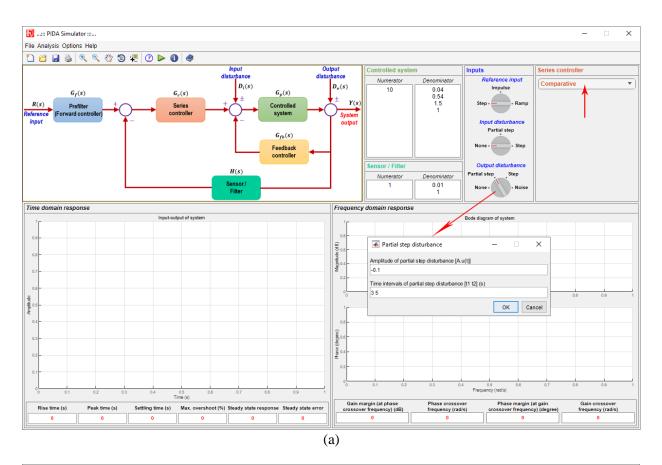
$$G_{c_{PIDA}}(s) = K_{p} + \frac{K_{i}}{s} + K_{d}s + K_{a}s^{2}$$

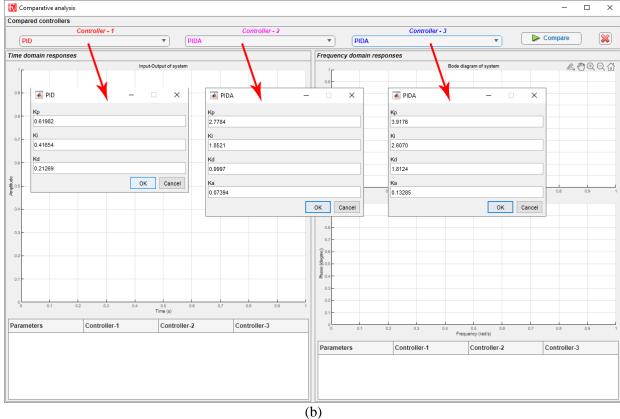
$$= 2.7784 + \frac{1.8521}{s} + 0.9997s + 0.07394s^{2}$$

$$G_{c_{PIDA}}(s) = K_{p} + \frac{K_{i}}{s} + K_{d}s + K_{a}s^{2}$$

$$= 3.9176 + \frac{2.6070}{s} + 1.8124s + 0.13285s^{2}$$
(11)

The input of the system parameters, reference input and partial step output disturbance is shown in Figure 4a. When "Comparative" is selected as the controller, the screen in Figure 4b opens and the parameters of the controllers to be compared are entered. By clicking the "Compare" button, the comparative simulation results are displayed as in Figure 4c. Here, in addition to the time and frequency domain comparative system responses, the relevant domain parameters are also displayed in tables. Thus, it is presented to the user which controller is more suitable for the defined system.





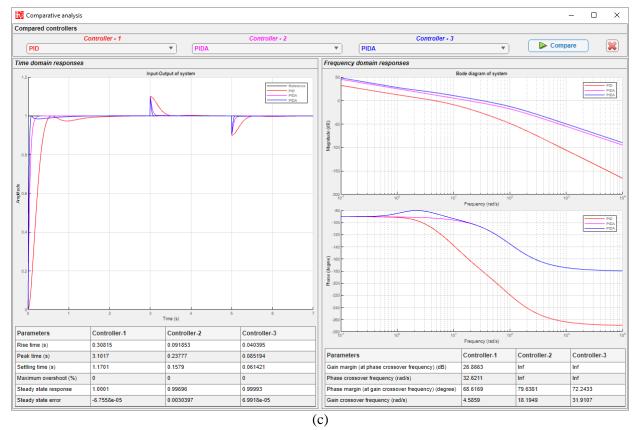


Figure 4. The screenshots for second application (İkinci uygulama için ekran görüntüleri)

## Case study-3: Induction motor system

In the third application, a cascade control structure is used to control a simplified asynchronous motor model obtained by neglecting the electrical subsystem model. An Integral-Proportional (I-P) controller is used as speed controller in inner control loop, and a PIDA controller is used as position controller in outer loop. The following transfer function for the I-P controlled speed control loop is obtained by using the model parameters given in [4, 8, 55]; where  $k_i$ ,  $k_p$  are the I-P controller parameters, and J, b,  $k_t$  are mechanical inertia constant, total friction coefficient, and torque constant of the motor, respectively.

$$G_p(s) = \frac{k_i k_t}{Js^2 + (b + k_i k_t)s + k_i k_t}$$

$$= \frac{168.0436}{s^3 + 25.921s^2 + 168.0436s}$$
(12)

In the third simulation, the performances of the following three PIDA controllers with H(s) = 1

(PIDA-Jung&Dorf, I-PDA, PIDA-Kitty) were compared.

$$G_{c_{PIDA-Jung\&Dorf}}(s) = K_p + \frac{K_i}{s} + K_d s + K_a s^2$$

$$= 11.8966 + \frac{19.5138}{s} + 2.445s + 0.1266s^2$$

$$G_{c_{I-PDA}}(s) = \begin{cases} G_c(s) = \frac{K_i}{s} = \frac{18.734}{s} \\ G_{fb}(s) = K_p + K_d s + K_a s^2 \\ = 8.842 + 0.669s + 0.0033s^2 \end{cases}$$

$$G_{c_{PIDA-Kitty}}(s) = K_p + \frac{K_i}{s} + K_d s + K_a s^2$$

$$= 5.6672 + \frac{9.3764}{s} + 0.7027s + 0.0248s^2$$
(13)

After the data entries are completed, the comparative simulation results screen shown as in Figure 5.

(12) The comparison of the developed simulator in this study with other simulators in the literature is summarized in Table 3.

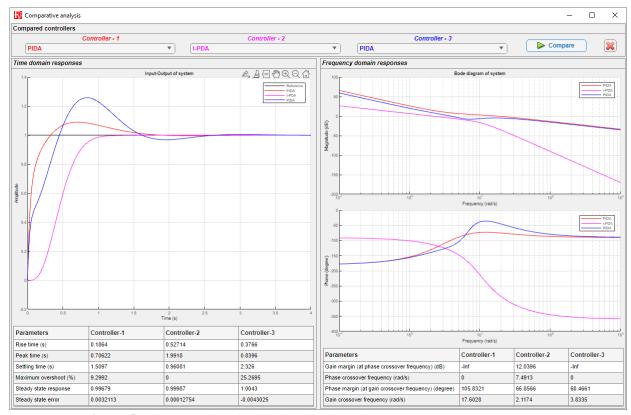


Figure 5. The screenshot for third application (Üçüncü uygulama için ekran görüntüsü)

 Table 3. The comparison of simulators' properties (Simülatör özelliklerinin karşılaştırılması)

| Properties                          |                        |                       | Ref.<br>[50] | Ref.<br>[51] | Ref.<br>[52] | Ref.<br>[53] | Ref.<br>[54] | This study |
|-------------------------------------|------------------------|-----------------------|--------------|--------------|--------------|--------------|--------------|------------|
|                                     | Step                   | 1                     | √<br>√       | √ V          | V            | 1            | 1            |            |
| Reference input                     | Impulse                |                       |              |              |              |              |              | √          |
| •                                   | Ramp                   |                       |              |              |              |              | V            |            |
|                                     | Step                   | √                     | <b>√</b>     | <b>√</b>     |              |              | 1            |            |
| Input disturbance                   | Delayed step           |                       |              | $\sqrt{}$    |              |              |              |            |
| •                                   | Partial step           |                       |              |              |              |              |              |            |
|                                     | Step                   |                       |              |              |              |              | √            |            |
| Output disturbance                  | Partial step           |                       |              |              |              |              | √            |            |
| •                                   | Noise                  | √                     |              | √            |              |              | √            |            |
| Controlled system definition        |                        |                       |              |              | <b>√</b>     | <b>V</b>     |              |            |
| Sensor / Filter definition          |                        |                       |              |              |              |              |              |            |
|                                     | PID (P, PI, PD, PID)   | <b>√</b>              |              | <b>√</b>     | <b>V</b>     |              |              |            |
|                                     | PIDA                   | √                     | √            | √            | √            | √            | √            |            |
| Series controller                   | I-PD                   |                       |              |              |              |              | √            |            |
| Series controller                   | I-PDA                  |                       |              |              |              |              | √            |            |
|                                     | PID with prefilter     |                       |              |              |              |              | √            |            |
|                                     | PIDA with prefilter    |                       |              |              |              |              |              | √          |
| Analysis type                       | Single                 | √                     |              |              |              |              | √            |            |
| Analysis type                       | Comparative            |                       |              |              |              |              |              |            |
|                                     | Time domain plot       | Reference tracking    | <b>√</b>     |              | $\checkmark$ |              |              |            |
|                                     |                        | Disturbance rejection | <b>√</b>     |              | √            |              |              |            |
| Responses                           | Time domain parameters |                       |              |              |              |              |              |            |
|                                     | Frequency domain p     |                       |              |              |              |              |              |            |
|                                     | Frequency domain p     |                       |              |              |              |              | √            |            |
| ·                                   | Closed-loop transfer   |                       |              |              |              |              |              |            |
| System information                  | Pole-zeros listing     |                       |              |              |              |              |              |            |
| System information                  | Pole-zeros mapping     |                       |              |              |              |              |              |            |
|                                     | Root locus             |                       |              |              |              |              | √            |            |
| Basic operations (save, print etc.) |                        |                       |              |              |              |              |              | 1          |
| Basic graphical operations (zoom    | in, zoom out, etc.)    |                       |              |              |              |              |              | <b>√</b>   |

## 4. **CONCLUSIONS** (SONUÇLAR)

Traditional PID controllers are used in many industrial applications. However, as the degree of

the controlled system increases, the performance in the control process may decrease. This situation can be compensated by using PIDA controllers. In this study, an interactive simulator was designed for students and people who will use the relevant controllers in the control area. With this developed simulator, performance evaluations of PID and PIDA type controller systems can be easily performed with time and frequency domain analysis. In addition, the simulator has capabilities such as comparative analysis, analysis of input and output disturbance effects, and analysis of different reference input responses. In addition, it can provide detailed information about the relevant systems (closed-loop transfer functions, pole-zero mapping/listing, root locus plot, time and frequency performance parameters etc.). These features of the developed simulator are demonstrated with three case studies (maglev vehicle suspension system, automatic voltage regulator, induction motor system) performed in the work. As future work, it is planned to develop a web-based simulator and to extend by adding different controllers.

## **DECLARATION OF ETHICAL STANDARDS** (ETİK STANDARTLARIN BEYANI)

The author of this article declares that the materials and methods they use in their work do not require ethical committee approval and/or legal-specific permission.

Bu makalenin yazarı çalışmalarında kullandıkları materyal ve yöntemlerin etik kurul izni ve/veya yasal-özel bir izin gerektirmediğini beyan ederler.

## **AUTHORS' CONTRIBUTIONS** (YAZARLARIN KATKILARI)

*Metin Hatun*: Conceptualization, Methodology, Investigation, Validation, Visualization, and Writing.

Kavramsallaştırma, Metodoloji, Araştırma, Doğrulama, Görselleştirme ve Yazma.

*Fahri Vatansever*: Conceptualization, Methodology, Investigation, Software Development, Validation, Visualization, Writing-Reviewing and Editing.

Kavramsallaştırma, Metodoloji, Araştırma, Yazılım Geliştirme, Doğrulama, Görselleştirme, Yazma-İnceleme ve Düzenleme.

## **CONFLICT OF INTEREST** (ÇIKAR ÇATIŞMASI)

There is no conflict of interest in this study.

Bu çalışmada herhangi bir çıkar çatışması yoktur.

## **REFERENCES** (KAYNAKLAR)

- [1] O'dwyer, A., (2009). Handbook of PI and PID controller tuning rules (third edition). London: Imperial College Press.
- [2] Åström K.J. and Hägglund T. (2006). Advanced PID Control. ISA-The Instrumentation, Systems and Automation Society.
- [3] Jung S. and Dorf R.C., Novel analytic technique for PID and PIDA controller design, IFAC Proceedings Volumes, 29 No. 1 (1996) 1146-1151. doi: 10.1016/S1474-6670(17)57819-2
- [4] Jung S. and Dorf R.C., Analytic PIDA controller design technique for a third order system, Proceedings of 35th IEEE Conference on Decision and Control, (1996), 2513-2518. doi: 10.1109/CDC.1996.573472
- [5] Patu P., Jongkol L, Kitti T., Noriyuki K., Shunji M., PIDA controller design by CDM, Proceedings of the 13th KACC, (1998) 395-400.
- [6] Dal-Young H., Ihn-Yong L., Young-Seung C., Young-Do L., Boo-Kwi C., The design of PIDA controller with pre-compensator, 2001 IEEE International Symposium on Industrial Electronics Proceedings (ISIE 2001), (2001) 798-804. doi: 10.1109/ISIE.2001.931570
- [7] Ukakimaparn P., Pannil P., Boonchuay P., Trisuwannawat T., PIDA controller designed by Kitti's method, 2009 ICROS-SICE International Joint Conference, (2009) 1547-1550.
- [8] Sambariya D.K., Paliwal D., Comparative design and analysis of PIDA controller using Kitti's and Jung-Dorf approach for third order practical systems, Journal of Advances in Mathematics and Computer Science, 16 No. 5 (2016) 1-16. doi: 10.9734/BJMCS/2016/26223
- [9] Bhandari M. and Rathore A., Comparative study of design of PIDA controller for induction motor, 2016 10th International Conference on Intelligent Systems and Control (ISCO), (2016) 1-5. doi: 10.1109/ISCO.2016.7726968
- [10] Kumar M., Hote Y.V., Robust CDA-PIDA control scheme for load frequency control of interconnected power systems, IFAC-PapersOnLine, 51 No. 4 (2018) 616-621. doi: 10.1016/j.ifacol.2018.06.164

- [11] Jitwang T., Nawikavatan A., Puangdownreong D., Optimal PIDA controller design for three-tank liquid-level control system with model uncertainty by cuckoo search, International Journal of Circuits, Systems and Signal Processing, 13 (2019) 60-65.
- [12] Kumar M. and Hote, Y.V., Graphic RCRA-PIDA tuning based on maximum sensitivity for automatic generation control of thermal and hydro power systems, IET Generation, Transmission & Distribution, 14 No. 26 (2020) 6427-6439. doi: 10.1049/iet-gtd.2020.0618
- [13] Kumar M. and Hote, Y.V., Maximum sensitivity-constrained coefficient diagram method-based PIDA controller design: application for load frequency control of an isolated microgrid, Electrical Engineering, 103 No. 5 (2021) 2415-2429. doi: 10.1007/s00202-021-01226-4
- [14] Kumar M., Hote Y.V., Sikander A., A novel cascaded CDM-IMC based PIDA controller design and its application, 2023 IEEE IAS Global Conference on Renewable Energy and Hydrogen Technologies (GlobConHT), (2023) 1-7. doi: 10.1109/GlobConHT56829.2023.10087714
- [15] Sahib M.A., A novel optimal PID plus second order derivative controller for AVR system, Engineering Science and Technology, An International Journal, 18 No. 2 (2015) 194-206. doi: 10.1016/j.jestch.2014.11.006
- [16] Hlangnamthip S. and Puangdownreong D., Optimal PIDA controller design for Maglev vehicle suspension system by Lévy-Flight Firefly algorithm, SWU Engineering Journal, 14 No. 1 (2019) 12-22.
- [17] Romsai W. and Nawikavatan A., Optimal PIDA controller design for quarter car suspension system by intensified current search, 2019 17th International Conference on ICT and Knowledge Engineering (ICT&KE), (2019) 1-5. doi: 10.1109/ICTKE47035.2019.8966863
- [18] Kumar M. and Hote Y.V., A novel PIDA controller design for a single-axis gimbal system, 2020 IEEE 17th India Council International Conference (INDICON), (2020) 1-6. doi: 10.1109/INDICON49873.2020.9342153
- [19] Huba M., Bistak P., Vrancic D., Optimizing constrained series PIDA controller for speed loops inspired by Ziegler-Nichols, 2023

- International Conference on Electrical Drives and Power Electronics (EDPE), (2023) 1-6. doi: 10.1109/EDPE58625.2023.10274033
- [20] Huba M., Bistak P., Vrancic D., Parametrization and optimal tuning of constrained series PIDA controller for IPDT models, Mathematics, 11 No. 20 (2023) 4229. doi: 10.3390/math11204229
- [21] Huba M., Bistak P., Vrancic D., Series PIDA controller design for IPDT processes, Applied Sciences, 13 No. 4 (2023) 2040. doi: 10.3390/app13042040
- [22] Huba M., Bistak P., Brieznik J., Vrancic D., Constrained series PI, PID and PIDA controller design inspired by Ziegler-Nichols, Power Electronics and Drives, 9 No. 1 (2024) 331-346. doi: 10.2478/pead-2024-0021
- [23] Huba M., Filtered PIDA controller for the double integrator plus dead time, IFAC-PapersOnLine, 52 No. 27 (2019) 106-113. doi: 10.1016/j.ifacol.2019.12.741
- [24] Huba M. and Vrančič D., Comparing filtered PI, PID and PIDD<sup>2</sup> control for the FOTD plants, IFAC-PapersOnLine, 51 No. 4 (2018) 954-959. doi: 10.1016/j.ifacol.2018.06.099
- [25] Heo J.P., Lim S., Im C.G., Ryu K.H., Sung S.W., New non-interactive form of the proportional-integral-derivative-acceleration (PIDA) controller and its explicit tuning rule, Korean Journal of Chemical Engineering, 40 (2023) 1277-1283. doi: 10.1007/s11814-022-1356-0
- [26] Hu X., Hou G., Tan W., Tuning of PIDD2 controllers for oscillatory systems with time delays, Frontiers in Control Engineering, 3 (2023) 1-17. doi: 10.3389/fcteg.2022.1083419
- [27] Hu X., Tan W., Hou G., Tuning of PID/PIDD2 controllers for second-order oscillatory systems with time delays, Electronics, 12 No. 14 (2023) 3168. doi: 10.3390/electronics12143168
- [28] Li Z., Zhang H., Tan W., Comparison of PI/PID/PIDD2 controllers for higher order processes, IFAC-PapersOnLine, 58 No. 7 (2024) 340-345. doi: 10.1016/j.ifacol.2024.08.085
- [29] Hu X., Tan W., Hou G., PIDD2 control of large wind turbines pitch angle, Energies, 16 No. 13 (2023) 1-22. doi: 10.3390/en16135096

- [30] Kumar M. and Hote Y.V., Robust IMC-PIDA controller design for load frequency control of a time delayed power system, 2019 IEEE 58th Conference on Decision and Control (CDC), (2019) 8380-8385. doi: 10.1109/CDC40024.2019.9029259
- [31] Kumar M. and Hote Y.V., Robust PIDD2 controller design for perturbed load frequency control of an interconnected time-delayed power systems, IEEE Transactions on Control Systems Technology, 29 No. 6 (2021) 2662-2669. doi: 10.1109/TCST.2020.3043447
- [32] Anwar M.N., Siddiqui M.A., Laskar S.H., Yadav A., PIDA controller design for higher order stable process with inverse response characteristic, 2018 International Conference on Computational and Characterization Techniques in Engineering & Sciences (CCTES), (2018) 236-240. doi: 10.1109/CCTES.2018.8674158
- [33] Huba M. and Vrancic D., Extending the model-based controller design to higher-order plant models and measurement noise, Symmetry, 13 No. 5 (2021) 1-44. doi: 10.3390/sym13050798
- [34] Arulvadivu J., Manoharan S., Lal Raja Singh R., Giriprasad S., Optimal design of proportional integral derivative acceleration controller for higher-order nonlinear time system delay using m-MBOA technique, International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 35 No. 6 (2022)1-24. doi: 10.1002/jnm.3016
- [35] Visioli A. and Sánchez-Moreno J., IMC-based tuning of PIDA controllers: a comparison with PID control, IFAC-PapersOnLine, 58 No. 7 (2024) 1-6. doi: 10.1016/j.ifacol.2024.08.001
- [36] Visioli A. and Sánchez-Moreno J., A relay-feedback automatic tuning methodology of PIDA controllers for high-order processes, International Journal of Control, 97 No. 1 (2024) 51-58. doi: 10.1080/00207179.2022.2135019
- [37] Visioli A. and Sanchez-Moreno J., Design of PIDA controllers for high-order integral processes, 2023 IEEE 28th International Conference on Emerging Technologies and Factory Automation (ETFA), (2023) 1-8. doi: 10.1109/ETFA54631.2023.10275379
- [38] Kumar M. and Hote Y.V., PIDD2 Controller design based on internal model control approach for a non-ideal DC-DC boost

- converter, 2021 IEEE Texas Power and Energy Conference (TPEC), (2021) 1-6. doi: 10.1109/TPEC51183.2021.9384954
- [39] Kumar M. and Hote Y.V. Real-time performance analysis of PIDD2 controller for nonlinear twin rotor TITO aerodynamical system, Journal of Intelligent & Robotic Systems, 101 No. 55 (2021) 1-16. doi: 10.1007/s10846-021-01322-4
- [40] Kumar M., Mahadeva R., Patole S.P., Non-fragile PIDA controller design for time-delayed uncertain system, IEEE Access, 12 (2024) 81156-81169. doi: 10.1109/ACCESS.2024.3411146
- [41] Zandavi S.M., Chung V., Anaissi A., Accelerated control using stochastic dual simplex algorithm and genetic filter for drone application, IEEE Transactions on Aerospace and Electronic Systems, 58 No. 3 (2022) 2180-2191. doi: 10.1109/TAES.2021.3134751
- [42] Saikia L.C. and Sinha, N., Automatic generation control of a multi-area system using ant lion optimizer algorithm based PID plus second order derivative controller, International Journal of Electrical Power & Energy Systems, 80 (2016) 52-63. doi: 10.1016/j.ijepes.2016.01.037
- [43] Kumar M., Altaf A., Biswas, D., Cheetah optimizer based PIDA controller design for cyber-physical power system under cyberattacks and uncertainty, 2023 IEEE 3rd International Conference on Smart Technologies for Power, Energy and Control (STPEC), (2023) 1-5. doi: 10.1109/STPEC59253.2023.10431040
- [44] Kumar M., Resilient PIDA control design based frequency regulation of interconnected time-delayed microgrid under cyberattacks, IEEE Transactions on Industry Applications, 59 No. 1 (2023) 492-502. doi: 10.1109/TIA.2022.3205280
- [45] Huba M., Vrancic D., Bistak P., PID control with higher order derivative degrees for IPDT plant models, IEEE Access, 9 (2021) 2478-2495. doi: 10.1109/ACCESS.2020.3047351
- [46] Bistak P., Huba M., Vrancic D., Chamraz S., IPDT Model-Based Ziegler–Nichols Tuning Generalized to Controllers with Higher-Order Derivatives, Sensors, 23 No. 8 (2023) 3787. doi: 10.3390/s23083787
- [47] Huba M., Vrancic D., Bistak P., Series PID control with higher-order derivatives for

- processes approximated by IPDT models, IEEE Transactions on Automation Science and Engineering, 21 No. 3 (2024) 4406-4418. doi: 10.1109/TASE.2023.3296201
- [48] Huba M., Bistak P., Brieznik J., Vrancic D., Constrained series PI, PID and PIDA controller design Inspired by Ziegler-Nichols, Power Electronics and Drives, 9 No. 1 (2024) 331-346. doi: 10.2478/pead-2024-0021
- [49] Vrančić D. and Huba M., High-order filtered PID controller tuning based on magnitude optimum, Mathematics, 9 No. 12 (2021) 1-24. soi: 10.3390/math9121340
- [50] Bisták P., Disturbance analysis virtual laboratory for PID controllers with higher derivative degrees, 2018 16th International Conference on Emerging eLearning Technologies and Applications (ICETA), (2018) 69-74. doi: 10.1109/ICETA.2018.8572065
- [51] Bisták P. and Huba M., Analysis of higher derivative degree PID controllers via virtual laboratory, 2019 27th Mediterranean Conference on Control and Automation (MED), (2019) 356-361. doi: 10.1109/MED.2019.8798527
- [52] Bisták P. and Huba M., Interactive software tool for design of higher derivative degree PID controllers, IFAC-PapersOnLine, 53 No. 20 (2020) 17198-17203. doi: 10.1016/j.ifacol.2020.12.1736

- [53] Ferrari M. and Visioli A., A software tool to understand the design of PIDA controllers, IFAC-PapersOnLine, 55 No. 17 (2022) 249-254. doi: 10.1016/j.ifacol.2022.09.287
- [54] Žáková K., Matišák J., Šefčík J., Contribution to PID and PIDA interactive educational tools, IFAC-PapersOnLine, 58 No. 7 (2024) 115-119. doi: 10.1016/j.ifacol.2024.08.020
- [55] Numsomran A., Sriratana W., Julsereewong P., Kongratana V., Tirasesth K., I-PDA controller designed by CDM, Proceedings of the 14th KACC, (1999) 284-287.
- [56] The MathWorks Inc. (2022). MATLAB, App Designer.
- [57] Bingül Z. and Karahan O., A novel performance criterion approach to optimum design of PID controller using cuckoo search algorithm for AVR system, Journal of the Franklin Institute, 355 No. 13 (2018) 5534-5559. doi: 10.1016/j.jfranklin.2018.05.056
- [58] İzci D., Ekinci S., Mirjalili S., Optimal PID plus second-order derivative controller design for AVR system using a modified Runge Kutta optimizer and Bode's ideal reference model, International Journal of Dynamics and Control, 11 No. 3 (2023) 1247-1264. doi: 10.1007/s40435-022-01046-9