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IMPROVING THE SHEET RESISTANCE OF CVD-GRAPHENE FILMS VIA DOPING

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ABSTRACT

We report on the synthesize high quality and large area graphene and modifying their electrical properties by non-covalent doping methods. Large area graphene film was synthesized by chemical vapor deposition method on pre-treatment Cu foil and demonstrated to be greatly efficient to the quality of graphene. The films are mostly monolayer with the transmittance of about 98%. Graphene film was transferred onto Si/SiO₂ substrate followed by various Self-Assembled Monolayers (SAMs) solution dropping. After doping, the number of charge carrier changes due to the nature of SAMs. Electrical measurement shows that doping method can effectively manipulate the electrical properties of graphene and also doped graphene sheets can use in optoelectronics, solar cells, and thermoelectric solar cells etc.

Keywords: Graphene, CVD, Transparent conducting films, Doping, Electrical properties

1. INTRODUCTION

Graphene, two-dimensional form of crystallized carbon, has attracted great interest since 2004 due to its outstanding electrical, mechanical, chemical and optical properties [1, 2]. Experimentally isolated single-layer graphene attracted a large amount of attention in physics and material science. Graphene with extraordinary electronic properties due to its unique band structure opens a way for using in the electronic application such as field-effect transistors (FETs), supercapacitors and transparent conducting films [3, 4]. The development of graphene-based electronics depends on our ability to modulation of its electronic properties and band structure. Graphene energy band structure is suitable for p-type or n-type chemical doping, for instance after doping with B or N atoms, graphene become p-type or n-type, respectively [5-7]. The chemical doping, using heteroatoms or covalent bonds with specific functional groups or even other molecules, provides charge transfer or opening a band gap around the Fermi level and also modification of the carbon sp^2 lattice [8]. Theoretical and experimental studies have shown that chemical doping alter the Dirac point of graphene above the Fermi level and can reduced sheet resistance of graphene films [9-12]. Therefore, graphene can be utilized as transparent and flexible conducting film for variety of applications. In addition, the synthesis of low-defect graphene is crucial to develop largearea graphene-based applications. On the other hand, the synthesis of low-defect graphene is crucial to develop large-area graphene-based applications. Chemical vapor deposition (CVD) synthesis is a reliable and scalable method for producing large area graphene [13-15].

Self-assembled monolayers (SAMs) are a one molecule thick, well-oriented molecular structures that are formed onto a surface by the adsorption of an active surfactant. SAMs are used as interlayers and electrically active layers to obtain charge carrier injection and also SAMs are a good candidate for using in organic electronics owing to flexibility, inexpensively, versatility and changeable physical properties [16-18]. SAM modification of graphene electronically inactivates graphene edges and defects and also manipulate the number of charge carriers in graphene so that the graphene's conductivity could be enhanced ensures graphene to be utilized as transparent and flexible conducting film for the variety of applications.

*Corresponding Author: <u>fethullah.gunes@ikc.edu.tr</u> Received: 14.04.2018 Accepted: 23.09.2018 In this work, development of electrically conductive graphene structures are targeted. Graphene was synthesized on pre-treatment Cu foil by CVD method and transferred onto another substrate followed by 10⁻⁴ M SAM molecules doping. Structure and distribution of the fabricated graphene were studied by Raman Spectroscopy, SEM characterization techniques and Atomic Force Microscopy (AFM). The influence of doping on the electrical properties of graphene were measured by Hall-effect measurement device.

2. EXPERIMENTAL

2.1. Synthesis of Graphene Film and Transfer Process

Firstly, commercially available Cu foils were pre-annealed and chemically-mechanically polished (CMP) for reducing the impurity or defect density on copper foils. To investigate the effect of the roughness, graphene was grown on unpolished and polished Cu foil using atmospheric pressure CVD. The foil was placed in a CVD quartz tube with a flow of hydrogen (H₂, 80 sccm) and argon (Ar, 1000 sccm). The temperature was increased from room temperature to 1079 °C and annealed 30 min. with the same gas atmosphere. Graphene was synthesized at 1079 °C by a carbon source methane gas flow for 1 min. and then chamber cooled to room temperature in the Ar/H₂ atmosphere. After synthesis, poly (methyl methacrylate) (PMMA) was spin-coated onto the graphene films and immersed into Cu etchant (FeCl₃) solution. After Cu was completely etched away, the graphene films with PMMA rinsed with deionized (DI) water several times and then, PMMA-coated graphene sheets were transferred onto a Si/SiO₂ substrate. The sample was finally dipping into hot acetone to remove the PMMA layer and dried using a nitrogen gun.

2.2. Preparation of Doped Graphene Film

4-Fluorophenyl boronic acid (F1-PBA), 3,4,5 Trimethoxyphenyl boronic acid (M3-PBA), 4,4"bis(diphenylamino)-1,1':3',1"-terphenyl-5'-carboxylic acid (TPA) and 4,4"-di-9H-carbazol-9-yl-1,1':3',1"-terphenyl-5'-carboxylic acid (CAR) aromatic molecules were dissolved in Dimethyl sulfoxide (DMSO) for preparing 10^{-4} M solution. Transferred graphene onto Si/SiO₂ were kept in prepared SAMs solution for 24 hours to be coated with SAMs. Finally, the substrates were rinsed with DMSO to remove some SAMs residues and dried using a nitrogen gun.

2.3. Characterizations

Sheet resistance measurements were performed by a Hall-effect measurement system (ECOPIA, HMS 3000) at room temperature. Optical properties of graphene were characterized using UV-vis spectroscopy (Thermo Scientific, Evolution Array) and Raman spectroscopy (Renishaw, 532 nm). Structure of graphene were studied SEM (Zeiss Sigma 300VP) and AFM (Nanosurf, easyscan 2) characterization techniques.

3. RESULTS AND DISCUSSION



Figure 1. Schematic representation for the graphene synthesis

Figure 1 shows a schematic of synthesis strategy. Optical microscopy and AFM analysis were used to understand effect of pretreatment on Cu. Optical images shows that the surface morphology of bare Cu

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foil is dominated by striation lines (Figure 2a). After CMP, the morphologies were mostly reformed and grain boundaries became more pronounced while the structural defect lines are reduced (Figure 2b). 3D AFM images of unpolished and polished Cu show that polished Cu surface more flatten than unpolished Cu (Figure 2 c and d). Root mean square (RMS) roughness parameter was used to measure the roughness degree of Cu surface. The RMS values of unpolished and polished Cu were calculated 138 nm and 7 nm, respectively. It is clear that unpolished Cu has relatively rough surface than polished Cu. These results are consistent with earlier reports indicating that annealing and polishing of Cu reduces surface contamination and obtain a flat surface [19, 20]. However, CMP method for the preparing metal substrate before graphene growth is rarely reported. Our procedure is a simple method to polish the Cu foils, which here surface roughness was decreased to 7nm.

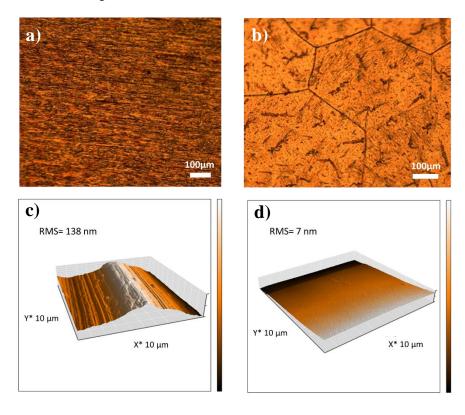


Figure 2. Surface morphology characteristics (a), (b) Optic microscopy, (c), (d) 3D AFM image of as-received Cu foil (a, c) and polished Cu (b, d), roughness mean square is abbreviated as RMS.

To demonstrate the effect of Cu foil on graphene quality, graphene film was synthesized on both unpolished and CMP Cu foils and transferred onto Si/SiO₂ substrate. Figure 3a and 3b are the transferred graphene on Si/SiO₂ substrate from unpolished and CMP Cu foil, respectively. Impurities on the unpolished Cu act as nucleation centers and graphene prefer growth around this centers. The graphene which was grown on unpolished Cu was nucleated randomly and contained incomplete areas due to the existence of impurity particles (Figure 3a). With CMP process, impurities of Cu were decreased effectively and thus the number of graphene domains was decreased (Figure 3b).

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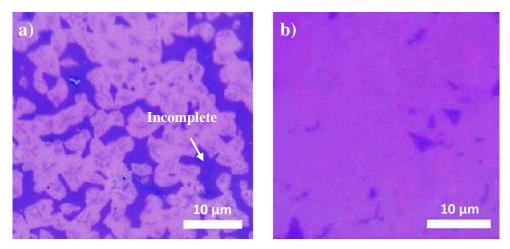


Figure 3. Optical microscopy image of graphene grown on (a) unpolished and (b) polished Cu.

The uniformity of the thin graphene layers was investigated by the Raman spectroscopy and SEM. For this purpose graphene film was synthesized on polished Cu and transferred on Si/SiO₂ substrate. Raman spectrum indicates typical appearance of transferred graphene on Si/SiO₂ including G (1586 cm⁻¹) and 2D band (2700 cm⁻¹) respectively (Figure 4a). The G band is the first Raman peak is related to C-C stretching of sp² carbon, the 2D band is the second order graphene peak. Intensity ratio of 2D/G have been utilized to identify to number of graphene layers and I_{2D}/I_G was calculated about 2.74 verifying the presence of monolayer graphene. As seen from Figure 4b, some wrinkles were observed due to the difference their thermal expansion coefficients between graphene and metal throughout cooling [21, 22].

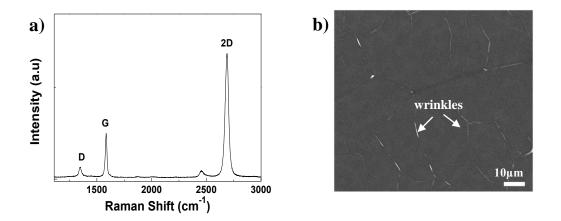


Figure 4. a) Raman spectroscopy b) SEM image of transferred graphene film on Si/SiO₂ substrate.

As a next step, graphene films were doped using various SAM molecules in order to examine the modification of its electrical properties. The SAMs form densely packed monolayer on graphene and interact with graphene by van der Waals interaction without damage to its chemical structure. Raman analysis and Hall-effect measurement were performed to understand the doping effect. The doping effect was visible in the G- and 2D-band shift. In the p-type of doping, the G band position upshifts due to the phonon stiffening effect by charge extraction. In the n-type doping, the G band position downshifts [23-25]. Doped graphene film G band has shifted about 3 cm⁻¹ from 1586 cm⁻¹ to 1583 cm⁻¹ as shown in Figure 5b. These result demonstrated that the graphene was effectively doped by SAMs.

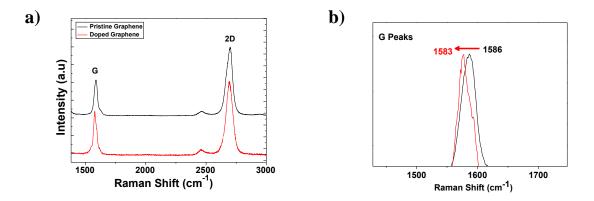


Figure 5. Raman spectra of (a) pristine (top) and doped (bottom) graphene film. (b) The shifted G band peak of graphene

The influence of SAM doping on the electrical properties of graphene were measured by Hall-effect measurement and summarized in Table1. The sheet resistance of undoped graphene was found about 770 ohm/sq at a transmittance 98%. The mobility of pristine graphene was measured about 1000 cm²/V.s. This values are consistent for CVD grown graphene as reported in the literature [26-29]. With SAMs doping, the sheet resistance was improved compared to that of the pristine graphene. The reducing in mobility after doping indicates that the graphene becomes n-type. Due to electron-donor nature of SAMs, electron transfer is occurred from SAM molecules to graphene due to π - π interaction. As a result sheet resistance of graphene increased and mobility decreased so that graphene became n-type. With this method, sheet resistance and mobility of graphene were modified with little sacrifice in transmittance.

Dopant	Rs (ohm/sq)	Mobility (cm²/V.s)	Т %
pristine graphene	770	1000	98
F1-PBA	1355	464	95.2
M3-PBA	1110	858	96
TPA	2011	584	97.5
CAR	1848	268	97.9

Table 1. Sheet resistance (Rs) mobility and transmittance (T%) of pristine and doped graphene.

4. CONCLUSION

In this study, it was presented a pre-annealing and chemical-mechanical polishing (CMP) method to reduce defect density on Cu foil and to enhance the quality of graphene. Using this method, large area, high-quality graphene film was synthesized on CMP Cu foil. The presence of the high-quality monolayer graphene was confirmed by Raman spectroscopy and Hall-effect measurements. In order to modify electrical properties, graphene films were doped with various SAM molecules. It is founded that the mobility and the sheet resistance of graphene can be controlled by SAM modification with little sacrifice in transmittance, which makes graphene n-type. The doping approach described here offers that electronic properties of graphene can be modified with SAM by simple dipping in a solution without using a plasma or high-temperature annealing. We believed that this study could open up a wide range of possibilities for electronic and optoelectronic applications

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