

# COMPARATIVE STUDY OF SINGLE-PHASE PWM RECTIFIER CONTROL TECHNIQUES

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## Abstract

In this study, 2.5 kW single-phase pulse-width modulated rectifier is simulated with three different control techniques to investigate the performance of controllers. Rectifier simulation is performed in Matlab / Simulink environment by using hysteresis current control, sinusoidal pulse width modulation and voltage oriented control techniques. In the performance comparison of the control techniques, considering the switching frequencies, the total harmonic content of the current drawn from the grid, the phase difference between the grid voltage and the grid current, and the DC bus voltage regulation at the output are considered as comparison criteria. The switching frequency is set to 35 kHz in sinusoidal pulse width modulation and voltage oriented control techniques. Since the switching frequency is variable in the hysteresis current control technique, the average and instantaneous switching frequency are calculated for different hysteresis band values. In the results with the technique, the switching frequency varies between 18.52 kHz and 47.6 kHz, while the average switching frequency is 34.6 kHz. As a result, the total harmonic distortion of the grid current with hysteresis current control, sinusoidal pulse width modulation and voltage oriented control techniques is 3.69%, 1.12% and 1.82%, respectively. The synchronization with the grid voltage is achieved with all techniques, and the DC voltage is regulated with active power.

Keywords: Hysteresis current control, Sinusoidal PWM, Rectifier

# TEK FAZLI PWM DOĞRULTUCU KONTROL TEKNİKLERİNİN KARŞILAŞTIRILMASI

## Özet

Bu çalışmada, 2.5kW gücünde tek fazlı darbe genişlik modülasyonlu doğrultucunun üç farklı kontrol yönteminde simülasyonu yapılarak bu yöntemlerin performansı incelenmiştir. Histerezis akım kontrolü, sinüzoidal darbe genişlik modülasyonu ve gerilim odaklı kontrol yöntemleri kullanılarak Matlab/Simulink ortamında doğrultucu simülasyonu gerçekleştirilmiştir. Kontrol yöntemlerinin performans karşılaştırmalarında anahtarlama frekansları dikkate alınarak şebekeden çekilen akımın toplam harmonik içeriği, şebeke gerilimi ile arasındaki faz farkı ve çıkıştaki DC bara gerilimi regülasyonu karşılaştırma kriterleri olarak ele alınmıştır. Sinüzoidal darbe genişlik modülasyonu ve gerilim odaklı kontrol yöntemlerinde anahtarlama frekansı 35 kHz seçilmiştir. Histerezis kontrol yönteminde anahtarlama frekansı değişken olduğundan dolayı farklı hata bandı değerleri için anahtarlama frekans değişimi hesaplanarak ortalama ve anlık anahtarlama frekansı tespit edilmiştir. Histerezis akım kontrol yönteminde anahtarlama frekansı 18,52 kHz ile 47,6 kHz aralığında değişirken ortalama anahtarlama frekansı 34,6 kHz olmuştur. Yapılan çalışma sonucunda histerezis akım kontrolü, sinüzoidal darbe genişlik modülasyonu ve gerilim odaklı kontrol yöntemlerinde şebeke akımı toplam harmonik bozulumu sırasıyla %3,69, %1,12 ve %1,82 olmuştur. Tüm yöntemlerde şebeke ile senkronizasyon sağlanmış ve DC bara gerilimi regülasyonu şebekeden aktif güç çekerek gerçekleşiririlmiştir.

Anahtar Kelimeler: Histerezis akım control, Sinüzoidal PWM, Doğrultucu Cite

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#### 1. Introduction

Rectifier circuits are used in many industrial applications such as uninterruptible power supplies [1], wind turbines [2], electric vehicle charging systems [3], DC motor control [4], active filter [5], electric

locomotive [6] and STATCOM [7] applications. Depending on the usage power, single-phase rectifiers are used at low power, and three-phase rectifiers are used at high power applications [8]. Three-phase rectifiers have some advantages such as higher output voltage, lower ripple, higher overall efficiency and simple filtering [9]. Rectifiers using diodes are called uncontrolled rectifiers, while rectifiers that use controlled semiconductors such as MOSFET, IGBT, thyristor are also called controlled rectifiers. Since MOSFET and IGBT are forced commutated elements, pulse width modulation (PWM) can be applied, and current in the desired waveform can be drawn from the AC side of the rectifier. Thus, low harmonic current and high power factor are obtained. Thyristor is used as a switching element in very high power applications. However, because the thyristor is self-commutated, pulse width modulation cannot be used, and the current harmonic content is higher [10].

L, LC or LCL filter is used for filtering the current drawn from the AC source in rectifier circuits [11]-[13]. While L filter provides advantage in terms of design simplicity, it is weaker in terms of harmonic attenuation compared to LC and LCL filters. LC filter is used to reduce the grid voltage and current ripple. It has more filtering capability than L filter. There is a resonance risk between filter capacitor and grid inductor depending on the connection point. Another drawback of the filter is inrush current [14]. Although the LCL filter has a more effective filtering feature, it has some difficulties at the point of design. Resonance is suppressed by taking this situation into consideration in the design of the controller or by using the resistance in the filter, since there may be resonance in the filter consisting of inductance and capacitor elements. However, in this case, additional power loss occurs on the resistance. In order to prevent this power loss, different methods are used in the control algorithm and the algorithms become complex [15].

In controlled rectifiers, the current drawn from the AC source should be synchronized with the grid, in other words, the grid synchronization should be done with high power factor. Therefore, the grid angle must be determined. The Phase-locked Loop (PLL) method is a popular method used in grid angle detection [16].

In this study, the simulation of a PWM controlled singlephase grid-connected rectifier is carried out in Matlab/Simulink environment to investigate the controller performances. Using the hysteresis current control (HCC), sinusoidal PWM (SPWM) and voltage oriented control (VOC) to rectifier, the performance of the control techniques on the rectifier has been presented comparatively. Performances of control techniques are compared in terms of total harmonic content, switching frequency, grid synchronization and DC voltage regulation. Since switching frequency is different in HCC control technique, average switching frequency value is calculated, and compared with other control techniques.

## 2. System Description

The simulated system in the study is seen in Figure 1. The rectifier consists of four IGBT switches connected to the single-phase grid via inductor. The inductor is used to filter the grid current. In order to provide bidirectional current flow, parallel connected reverse diodes are used in the IGBTs. Electrolytic DC capacitor is used for voltage filtering at the output of the rectifier to provide constant voltage. A load is used at the output to model the power drawn from the rectifier. Depending on the application, the output can be a DC motor, DC/DC converter or an inverter. Since the focus of the study is rectifier control, a load block in Simulink is used at the output.



Figure 1. The simulated system.

The equations of the mathematical model of the rectifier are given in (1)-(4). By using Kirchoff Voltage Law theorem, equation (1) can be written. In the equation, *S* is a switching function. It changes with states of upper switches as -1 and 1. DC side of the rectifier can be modeled with equations (2)-(4). In the equations,  $v_g$  and  $v_{dc}$  are grid and DC output voltage, respectively.  $i_{dc}$  is DC output current of converter.  $i_c$  and  $i_{Load}$  define capacitor and load currents.  $R_L$  is an inductor resistance, and  $i_g$  is grid current. Converter output current can be defined with *S* function [17].

$$v_g = L_{ac} \frac{di_g}{dt} + R_L i_g + S v_{dc} \tag{1}$$

$$\dot{i}_{dc} = \dot{i}_c + \dot{i}_{Load} \tag{2}$$

$$i_{dc} = Si_g \tag{3}$$

$$i_c = C_{dc} \frac{dv_c}{dt} \tag{4}$$

## 3. Control of Single-Phase PWM Rectifier

In controlled converters connected to the grid, the angle between the grid current and the grid voltage should be controlled. Thus, the power factor can be adjusted, and reactive power control can be made when it is required. After the grid angle is determined, the grid current is produced by the control algorithm depending on the reference current value, and switching signals are produced with the preferred modulation technique. In this study, rectifier control is performed by using hysteresis current control technique, sinusoidal PWM technique and voltage oriented control technique. In the controlled rectifier control, it is aimed to ensure that the currents drawn from the grid are sinusoidal and synchronized with the grid voltage, and the DC bus voltage regulation is ensured. For this reason, in each technique, the grid current value that provides DC bus voltage regulation is calculated first. Then, with the techniques described below, currents at this reference value are drawn from the grid. As seen in Figure 2, the grid voltage, the grid current and the DC bus voltage is measured to ensure synchronization, control, and regulation, respectively. The same circuit and measurements are used in all three techniques in the study. The parameters used in the all control techniques are given in Table 1. Switching frequency value is not fixed for HCC because it has variable frequency. Hysteresis band value is only used in HCC.



Figure 2. Single-phase PWM rectifier circuit.

Table 1. Circuit paramete	rs
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Parameter	Value
Grid voltage ( $V_g$ )	220V
Rectifier Power ( $P_0$ )	2.5 kW
Filter inductance ( <i>L</i> <sub>ac</sub> )	3mH
DC capacitor ( $C_{dc}$ )	4700µF
Switching frequency (f <sub>sw</sub> )	35 kHz
Hysteresis Band (∆i)	0.6 A

The used PLL method for grid angle determination is Synchronous Reference Frame PLL (SRF-PLL) in this study. SRF-PLL block diagram is seen in Figure 3.



Figure 3. SRF-PLL block diagram.

This method is applied in Stationary Reference Frame that has  $\alpha$  and  $\beta$  components and Synchronous Reference Frame that has *d* and *q* components. In this method, *q* component of the grid voltage is passed through low pass filter, and the output is added to fundamental frequency. Integrating the sum of frequencies gives the grid angle. It is used for  $\alpha$ - $\beta$ /*d*-*q* transformation. In three phase systems,  $\alpha$ - $\beta$ components are obtained from *a*-*b*-*c* components. However, the  $\beta$  component of a signal is provided by delaying the signal by  $\pi/2$  in single-phase systems.  $\beta$  component is produced with 90<sup>0</sup> phase difference with  $\alpha$  component. *A*- $\beta$ component are transformed into *d*-*q* frame by (5). The PI parameters used in the simulation in the PLL algorithm are k<sub>p</sub>=2 and k<sub>i</sub>=1.

$$\begin{bmatrix} v_d \\ v_q \end{bmatrix} = \begin{bmatrix} \cos \omega t & \sin \omega t \\ -\sin \omega t & \cos \omega t \end{bmatrix} \begin{bmatrix} v_\alpha \\ v_\beta \end{bmatrix}$$
(5)

The calculated grid angle variation is given in Figure 4. As seen in the figure, the calculated angle is synchronized with

grid voltage. It is the main issue in grid connected converters.



#### **3.1. Hysteresis Current Control**

The hysteresis current control is a control technique that controls current directly, and is preferred in terms of application simplicity and fast dynamic response [18]. In this control technique, the error between the reference current generated in the control algorithm, and the actual current is calculated. The relevant semiconductors in the converter are switched so that the current flows within the lower and upper limits determined for the error value. Figure 5 shows current error and switching signal variation. When the current error reaches the determined upper limit and lower limit, a signal is sent to the switching elements in the direction that will decrease or increase the current. Figure 6 shows the hysteresis current control technique algorithm. Here, the amplitude of the current to be drawn from the grid is obtained by passing the DC bus voltage error through the PI controller. DC bus voltage reference value is set to 400V in the study. The controller generates switching signals of the rectifier to produce grid current that provides DC bus voltage regulation to 400V. The measured grid voltage is passed through the PLL, the grid angle is determined, and the synchronous current reference with the grid is obtained. The current error is obtained by subtracting the grid current from the reference current, and switching is done depending on this error value. Since there is no carrier signal in this control technique, the switching frequency varies. When the current error reaches up to the upper limit, it means that the current reaches the lower limit, and for the current to increase, the S<sub>2</sub> and S<sub>3</sub> switches are given an ON signal, while the  $S_1$  and  $S_4$ switches are given an OFF signal. Thus, the current flows within the desired bandwidth [19].



Figure 5. Current error and switching signal in HCC.



#### Figure 6. HCC control algorithm.

The waveforms that show the performance of the system in steady state are seen in Figure 7. The parameters of PI controller are  $k_p=0.2$  and  $k_i=1.5$ . As seen in Figure 7a, the system has a 2.5 kW DC load. The controlled rectifier regulates the DC bus voltage to 400V, as shown in Figure 7b. In order to ensure this regulation, 11.4 A<sub>rms</sub> current is drawn from the grid as seen in Figure 7c. The grid current total harmonic distortion (THD<sub>1</sub>) value is measured 3.69% at 2.5kW nominal power. The harmonic content and waveform is given in Figure 8. As seen in Figure 7d, switching frequency varies in a period. The reason that the switching frequency variation is the voltage difference between the grid voltage and DC bus voltage. The difference voltage drops on the inductor, and it changes the slope of the grid current, and it causes frequency variation. As the difference has the highest value around zero voltage crossing points of grid voltage, it gets maximum value. The average switching frequency is 34.6 kHz.





Figure 7. Steady-state performance of the system.

Switching frequency of HCC changes depending on the hysteresis band value. Lower band value causes higher switching frequency but provides lower  $THD_I$  as seen in Table 2. As switching frequency is selected 35 kHz in SPWM and VOC techniques, hysteresis band value is set

to 0.6 A because average switching frequency is 34.6 kHz that is near the 35 kHz in the study.

Table 2. Switching frequency depending on t	he
hysteresis band value	

Hysteresis band value <i>(∆i</i> )	Average switching frequency(f <sub>sw</sub> )	THD <sub>1</sub>
0.6 A	34.6 kHz	3.69%
0.5 A	40 kHz	3.2%
0 25 A	66 92 kHz	1 92%



Figure 8. Harmonic measurement for  $\Delta i=0.6$  A

The transient response of the controller is also important to evaluate the system performance. For this reason, the load power of the system is changed in the simulation time interval. The DC bus regulation and grid current performance of the controller are examined in Figure 9. The load power shown in Figure 9a decreases from 2.5 kW to 2 kW at 1.2 seconds, and the power increases to 2.5 kW at 2 seconds. As seen in Figure 9b, the DC bus voltage changes depending on the load power change. The sudden decrease in the load causes increasing in the DC bus voltage. In this case, as seen in Figure 9c, the grid current is decreased by the controller to regulate the DC bus voltage. The opposite situation realized when the load power increases. The controller changes the grid current depending on the DC bus voltage. As can be seen from the figures, the controller provides DC bus voltage regulation by drawing sinusoidal and synchronized current with the grid voltage in steady-state and transient-state.





Figure 9. Transient response of the system.

#### 3.2. Sinusoidal PWM

In the sinusoidal PWM technique, unlike the HCC, switching signals are obtained by comparing a sinusoidal voltage with a triangle wave. The control algorithm is given in Figure 10. The reference current is generated by using the grid angle and reference grid current peak value that is obtained from the DC bus voltage error. The reference signal is generated by passing the current error through the proportional resonant (PR) controller. PWM signals are generated by comparing this signal with a triangle wave signal. This signal has 35 kHz frequency that determines the circuit operating frequency. Since a sinusoidal signal is used in this control method, PR controller is used instead of PI controller [20]. The load power, DC bus voltage and grid current waveforms are seen in Figure 11. As seen in the figures, controller regulates the DC bus voltage by drawing a sinusoidal and synchronized grid current. The total harmonic distortion of the grid current at 2.5 kW is  $THD_I = 1.12\%$  as seen in Figure 12.







Figure 12. Harmonic measurement of the grid current

As in the HCC control technique, load is changed to examine the transient performance of the control. As seen in Figure 13a, the load with a power of 2.5 kW decreased to 2 kW at 1.1 seconds and then increased to 2.5 kW again at the 2. seconds. As a result of this, as seen in Figure 13b, the DC bus voltage moves away from the reference value, and as seen in Figure 13c, the DC bus voltage regulation is achieved by adjusting the grid current. PI controller parameters are  $k_p=0.2$  and  $k_i=1.5$ , whereas  $k_p=0.1$  and ki=1 in PR controller. As in the HCC technique, in this control, a transition from transient to stable is achieved.





Figure 13. Transient performance of the system.

## 3.3. Voltage-Oriented Control

Unlike other control techniques, voltage-oriented control technique is carried out in *d-q* coordinate system [21]. As seen in Figure 14, firstly, the grid current is transformed to the  $\alpha$ - $\beta$  coordinate system. The  $\beta$  component lags behind the alpha component by 90 degrees. The  $\alpha$  component is the same as the grid current. This coordinate system is converted to the d-qcoordinate system, and thus the DC components of the grid current in the *d*-*q* coordinate system are obtained. Thus, error regulation can be performed with the PI controller. Since the grid current is desired to be synchronized with the grid voltage, the reference value of the q-component of the current is set to zero. The reference value of the *d*-component comes from the DC bus voltage regulation. This value refers to the peak value of the grid current. With the help of PI controllers, errors in *d-q* components are regulated, and reference d-q voltages are produced. These voltages that are transformed into  $\alpha$ - $\beta$  coordinate system using the grid angle and switching signals are obtained by comparing the  $\alpha$ -component with the triangle signal. The performance of the system with this control technique is shown in Figure 15. Depending on the output power seen in Figure 15a, DC bus voltage regulation is achieved at 2.5 kW output power by the rectifier as seen in Figure 15b. It is provided by the grid current that is seen in Figure 15c. The total harmonic distortion value of the grid current is measured 1.82% that meets the harmonic limits defined in standards as seen in Figure 16. The PI parameters for  $V_{dref}$  and  $V_{qref}$  are  $k_p=0.015$  $k_i {=} 0.15$  , whereas  $k_p {=} 0.2$  and  $k_i {=} 1.5$  for DC bus voltage PI controller.

The transient performance of the system is examined under the same conditions as the other techniques. As seen in Figure 17, the control technique has good transient performance such as the other techniques.



Figure 14. Voltage-oriented control algorithm



Figure 15. Steady-state performance of the system



Figure 16. Harmonic measurement of the grid current



Figure 17. Transient performance of the system.

## 4. Discussion

Three different control techniques, named hysteresis current control, sinusoidal PWM and voltage-oriented control, are applied to single-phase PWM rectifier. Table 3 shows the results of performance criteria of control techniques. As seen in the table, the switching frequency of HCC is different from other techniques because of variable switching frequency property of HCC. It depends on hysteresis band value. The switching frequency of HCC given in the table is average value (34.6 kHz) that occurs with 0.6 A hysteresis band value. The frequency varies between 18.52 kHz and 47.6 kHz. Although the controller algorithm is simple, the drawback of HCC is variable switching frequency and higher  $THD_1$  value. As seen in the table, the highest  $THD_I$  value is obtained in HCC technique. The other control techniques have lower  $THD_I$  value than the HCC. SPWM technique is applied with sinusoidal signals so PR regulator is used in the controller, whereas DC quantities are used in VOC technique. Because of the DC quantities, PI regulator is used in VOC technique. The sinusoidal signals are transformed to DC signals therefore higher computing power is required.

Table 3. Performance compa	rison of control	techniques
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Control Technique	Switching frequency (f <sub>sw</sub> )	Switching frequency type	THD <sub>I</sub>	DC bus voltage overshoot- undershoot
НСС	34.6 kHz	Variable	3.69%	3.1% - 3%

SPWM	35 kHz	Fixed	1.12%	3.1% - 3%
VOC	35 kHz	Fixed	1.82%	2.2% - 2.3%

# 5. Conclusion

In this study, the performance of single-phase PWM rectifier that is connected to a single-phase grid for three different control techniques is investigated. 400 V DC bus voltage is generated at the output of the rectifier with the power of 2.5 kW. Hysteresis current control technique, sinusoidal pulse width modulation technique and voltage-oriented control technique are used in the converter control. The system performance is investigated for steady-state and transient-state. In both state, all techniques perform inadequate performance in terms of harmonic content and power factor. The generated grid current is synchronized with grid voltage for three techniques. Total harmonic distortion value of the grid current is 3.69%, 1.12% and 1.82% for HCC, SPWM and VOC techniques, respectively. The results obtained in all three control techniques show that three techniques can be used in such a system. HCC technique has more simple control algorithm than the others but because of variable switching frequency and lower THD performance, the other control techniques are more efficient for the rectifier.

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