



High Voltage Gain Multi-port Bidirectional DC-DC Converter with an Effective Multi-loop Control Strategy for PV/Battery Integrated Systems

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Abstract

This study proposes a novel isolated bidirectional multi-port converter (MPC) based on a switched-capacitor converter and a half-bridge converter with an effective control scheme for photovoltaic (PV) powered and battery buffered systems. The proposed power electronics converter interface integrates the converters to the ports which are connected with a battery coupled common DC busbar and high-frequency transformer (HFT). Thus, the three-port converter is formed without any need for an additional converter to regulate battery power flow. In addition, to transfer power from a low voltage PV energy unit to the battery and load, a single switch DC-DC converter with high voltage gain is proposed. The power flow between the ports is controlled by an effective multi-loop control scheme that is able to perform a smooth transition between the loops. In order to validate the viability and effectiveness of the proposed MPC, a proof-of-concept simulation model has been developed with a 3 kW PV and 220 V 12 Ah battery. The performance of the proposed converter has been analyzed for different case studies, including dynamic operating and loading conditions.

Keywords: Multi-port DC-DC converter, switched-capacitor, photovoltaic, battery, multi-loop controller

1. INTRODUCTION

1.1. Overview

In the last few decades, the use and depletion of fossil fuels have accelerated in parallel with the significant increase in electricity demand [1]. The environmental concern and depletion of fossil fuels have led to develop renewable energy sources (RESs) powered clean applications such as electric vehicles [2], microgrids [3], residential grid-tie inverters [4]. Besides, RESs generating power in DC form are able to integrate with DC loads used in DC microgrids, which have become widespread in recent years, without using additional inverter devices [5]. Although RESs are good candidates for the aforementioned applications because of such advantages, they are by nature unstable in their availability and capacity [6]. The instantaneous production values of RESs vary with natural conditions such as solar irradiation/temperature [7], hydrogen pressure/temperature [8], and wind speed [9]. RES-based energy systems are frequently equipped with energy storage units in order to eliminate the non-linear behavior drawback of RESs. Battery units make it possible to provide uninterrupted power to loads by buffering fluctuations and interruptions of RESs.

The power flow between the RE- based energy unit, battery unit, and load is performed using power electronics converter interfaces. Traditionally, individual converters are employed and controlled in a decentralized manner to regulate the power flow [10]. Because of that, traditional RES-powered systems have high computational loads and costs. To overcome the aforementioned limitations, multi-port converters (MPC) are used, wherein RES-based energy units, battery units, and loads are connected to different ports of the MPC.

1.2. Literature Survey

MPCs are emerging power electronics solutions in order to integrate multi-inputs/outputs with combined functionalities and control schemes. MPCs can be divided into two categories as isolated and non-isolated converters and are presented in [11-13]. Non-isolated topologies have advantages such as simpler structure and compactness by the lack of a high-frequency transformer whereas they are not able to provide isolation and flexible voltage range [14]. On the other hand, isolated MPCs provide flexible voltage ranges via HFT turns ratio as well as isolation between the ports.

* Corresponding author Email: msavrun@atu.edu.tr	European Mechanical Science (2021), 5(3): 99-104 doi: https://doi.org/10.26701/ems.934248	
	Received: May 7, 2021 Accepted: June 1, 2021	はないの

Isolated MPCs are good candidates for RES powered and battery buffered systems because of their flexibility in performing power flow between the different ports.

Several isolated MPC topologies have been presented in the literature to integrate multi-input/output ports. Many studies have addressed isolated MPCs using the derivations of bridge converters that have capabilities of bidirectional power flow, galvanic isolation, zero-voltage switching, and zero-current switching [15]. Among the derivations of bridge converters, full-bridge converters have advantages of two times higher output voltage and higher power transfer capability, while half-bridge topologies with fewer switching devices are advantageous in relatively low power applications [16]. The turns ratio of HFTs makes it possible to provide flexibility in voltage conversion ratio, while the high turns ratio raises the problem of low efficiency. Therefore, MPCs that are endowed with low turns ratio HFTs and integrated with high gain converters stand out as in [7]. The power flow path at the MPC port where RESs are connected is designed to be unidirectional due to the nature of RESs. Because of that, RESs are equipped with unidirectional DC-DC converters. Various unidirectional high gain DC-DC converter topologies developed and applied in literature are reviewed in [17]. Step-up DC-DC converters temporarily store low voltage input energy on magnetic field storage components and transfer it to the output in high voltage levels [17]. The fundamental step-up converter is the traditional boost converters which have a restricted voltage gain with high output voltage ripple. In order to eliminate the output voltage ripples, interleaved topologies have been proposed [18]. Furthermore, multi-leg/multi-phase interleaved [19, 20], capacitor clamped h-bridge [21], and tristate boost converter [22] topologies are employed in order to further decrease the output voltage ripple and to improve the voltage conversion ratio. However, the related topologies have a limited gain and many active switching devices. The coupled inductor topologies make it possible to provide high voltage gain, but relatively large current ripples decay the service life of the connected port RES or battery unit [21]. Tang et al. [23] proposed a hybrid switched inductor converter. The switched inductor topology has a high voltage gain. However, the voltage stress on the diodes is the main drawback of the

related topology.

1.3. Key Contributions

Literature review reveals the current researches focused on MPCs in order to integrate multi-input/output ports with reduced switches and complexity. In addition, it is concluded that the unidirectional DC-DC converters that have a high voltage conversion ratio to extract maximum power from RESs play a critical role for RES-powered systems. Among the related DC-DC converter topologies, switched-capacitor boost converter and half-bridge converter excel with the aforementioned advantages.

This paper proposes a new MPC that is equipped with switched-capacitor DC-DC converter in order to integrate RES with an isolated DC-DC converter via a battery coupled common busbar. Since an additional DC-DC converter that regulates the power flow of the battery contributes significantly to the size and cost of the overall system, the power flow of the battery is regulated by the existing converters considering operation conditions. The proposed system is equipped with a multi-loop control structure that operates adaptively and allows instantaneous switching between the loops. In order to validate the viability and effectiveness of the proposed system, different load and temperature scenarios are operated.

The pattern of this study is organized as follows: the power circuit configuration of the proposed system is given in Section 2. In Section 3, the operation modes of the proposed MPC and overall control scheme are explained in detail. Performance analysis and case studies are investigated and discussed in Section 4. Finally, conclusions are presented in Section 5.

2. POWER CIRCUIT CONFIGURATION OF THE PROPOSED CONVERTER

The power circuit configuration of the proposed MPC which is illustrated in Figure 1 consists of a half-bridge converter, a controlled full-wave rectifier, a switched-capacitor DC-DC converter, and a secondary center-tapped high-frequency transformer (HFT). The proposed converter interface is purposed to integrate battery, PV, and load and supervise the optimal power flow between input and output ports.



Figure 1. Circuit design of the proposed MPC

The switched-capacitor DC-DC converter is used to transfer power from PV to the battery/load with high efficiency. Besides, the related DC-DC converter makes it possible to connect low voltage PV into a relatively high voltage battery due to its high voltage conversion ratio capability. Half-bridge converter allows bidirectional power flow by transferring power from input ports to the output port and from the output port to the battery. While the half-bridge converter operates as an inverter during forward power flow, it operates as an uncontrolled full-wave rectifier during reverse power flow. The galvanic isolation between the input and output ports is provided by HFT with low turns ratio. The controlled full wave rectifier located on the secondary side, on the contrary to the half-bridge converter, operates as an uncontrolled full-wave rectifier during the forward power flow and operates as an inverter during reverse power flow.

The proposed MPC integrates three different converters; (i) the switched-capacitor DC-DC converter, (ii) the half-bridge converter, and (iii) the controlled full-wave rectifier via battery coupled common DC busbar and HFT. On the primary side, while the battery operates as a common DC bus, the switched-capacitor DC-DC converter performs the maximum power point tracking, constant current (CC), and constant voltage (CV) charging methods. The DC-busbar of the half-bridge converter is fed by PV and buffered by battery. Thus, uninterrupted power transfer to the load is provided. The relationship between the PV and battery voltages is computed using the duty cycle of S₁. It is expressed as follows [24, 25];

$$Gain = \frac{V_{bat}}{V_{pv}} = \frac{2}{1-D}$$
(1)

where V_{bat} and V_{pv} represent the battery voltage and PV output voltage, respectively. *D* describes the duty cycle of switch S₁ and varies between 0-1 considering operation conditions of the system.

3. CONTROL SCHEME OF THE PROPOSED CONVERTER

The detailed control structure is shown in Figure 2. The main aims of the overall controller scheme are: (i) to ensure the continuity of the power transfer to the load with constant DC-link voltage, (ii) to transfer excessive power produced by PV to the battery, (iii) to provide bidirectional power flow between the ports. The effective control structure for the proposed MPC is a multi-loop control structure that is composed of power flow management, MPPT, CC/CV charging, and DC-link control. The system operates adaptively and allows instantaneous switching between the control loops.

The optimal power flow between input energy units and the output load with respect to the instantaneous PV generation, charge characteristics of the battery, and load variations are the main part of the proposed converter operation. According to the instantaneous states of the ports, there are three possible power flow circumstances as mode1, mode 2, and mode 3. Mode 1 corresponds to power transfer towards



the load via PV and battery. In this mode, the battery operates in discharging mode. In mode 2, PV feeds the load, whereas the battery is charged via PV. Mode 3 represents the reverse power flow that is towards from the load side to the battery. In this mode, the battery operates in charging mode.

In mode 1, the power of the load is provided from the PV and battery at the same time. There are two potential circumstances in this case. The primary condition may take place when the load power consumption is higher than the available PV power. In this time interval, the battery buffers the PV to transfer uninterrupted power to the load. The secondary condition is that the PV operates under rated power due to environmental situations such as irradiation (i.e. the generated power of PV decreases with irradiation reduction). The analytical relationship between power flows is given in Eq (2).

$$\int_{t_0}^{t_1} P_{load}(t) dt = \int_{t_0}^{t_1} P_{pv}(t) dt + \int_{t_0}^{t_1} P_{bat}(t) dt$$
(2)

In mode 2, the PV supplies battery, and the load, namely the power produced by PV feeds the load and charges the battery at the same time. This situation may occur under low load consumption ratings. The PV power could be decreased when the low power is consumed. However, in cases where the PV generation is insufficient to supply the load, the battery must be charged to keep the DC-link voltage constant by buffering the PV. In cases where the battery state of charge is less than 100%, the excessive power of the PV is used to charge the battery. During this time interval, power flows are expressed mathematically in Eq (3).

$$\int_{t_{1}}^{t_{2}} P_{load}(t) dt = \int_{t_{1}}^{t_{2}} P_{pv}(t) dt - \int_{t_{1}}^{t_{2}} P_{bat}(t) dt$$
(3)

In mode 3, the battery is charged from the load side. This case may occur under a condition where there is no solar irradiation and the battery is charged by the DC-microgrid to which the load is connected. By this way, the power flowing through the PV is zero. The power flow for this state is expressed as below:

$$\int_{t_2}^{t_3} P_{load}(t) dt = \int_{t_2}^{t_3} P_{bat}(t) dt \quad \& \quad \int_{t_2}^{t_3} P_{\rho\nu}(t) dt = 0$$
(4)

The power transfer towards the load is performed using the DC-link control loop continuously, while the latter control loops are enabled considering the instantaneous power production value of PV. The power transfer from the load side to the battery is performed using CC/CV charging algorithms. The common aspect of the aforementioned control loops is the use of the pulse width modulation (PWM) method. The duty cycle values of the switches regulate the power flow rating by adjusting the voltages of output DC-link and battery coupled DC busbar.

The DC-link control loop is used to regulate the output DC-link voltage by triggering S₂ and S₃ switching devices. The duty cycles of switches S₂ and S₃ with a 180° phase shift between each other varies within the limits of 0-50 %. A duty cycle of more than 50% causes the relevant switches to open simultaneously and short circuit. The duty cycles of the switches are computed using a PI controller. The difference between the reference (V_{dcref}) and actual voltages (V_{dc}) of the output load is applied to the PI controller.

The MPPT control loop is activated during mode 1 and some states of mode 2. The MPPT control loop is used to extract maximum power from PV by triggering the S_1 switching device. The maximum power tracking is performed by the perturb and observe (P&O) MPPT method due to the ease of operation and reduced computational load. The controller monitors the output voltage (V_{pv}) and current (I_{pv}) of PV and computes the instantaneous available power. The algorithm perturbs the operating voltage to ensure maximum power by adjusting the duty cycle of switch S_1 . The operating voltage perturbation is performed by computing the instantaneous duty cycle of S_1 switch as follows;

$$D_{new} = D_{old} + \Delta D \quad (if \ P > P_{old})$$
(5)

 $D_{new} = D_{old} - \Delta D \quad (if \ P < P_{old})$ (6)

$$D_{new} = D_{old} \quad (if \ P = P_{old}) \tag{7}$$

The available excessive power of PV is crucial in enabling one of the MPPT or CC control loops. The battery charging scenario has great importance to improve the service life of the battery. Therefore, the battery charge current is frequently is restricted with 0.5 C (C represents the nominal capacity of the battery) charging current. Considering the charging current limits of the battery, the related control loops are enabled. If the battery charge current reaches the charge current limit, the MPPT control loop switches to the CC control loop. The CC control loop rises the battery voltage to its reference value of 242 V. The CV control is enabled following the completion of the described operation of the CC loop. The CV charging continuous until the charging current gradually decreases to 0.05C. The CC/CV charging is achieved by adjusting the duty cycle of the switch S_1 . In addition, the CC/CV control loops are also enabled in mode 3, which is another power transfer state towards to battery. The duty cycle value of the S₁ switch is computed using PI controllers during related control loops are enabled. The CC control loop computes the duty cycle by using the error value between the reference $(I_{\textit{bref}})$ and actual $(I_{\textit{b}})$ values of the battery current while the CV control loop computes the duty cycle by using the error value between the reference (V_{bref}) and actual (V_{b}) values of the battery voltage.

4. PERFORMANCE ANALYSIS

In this section, to verify the effectiveness and viability of the proposed MPC and the controller, a 3 kW proof-of-concept model which consists of 3 kW PV and 220 V 12 Ah battery has been developed using MATLAB/Simulink. The parameters of the simulation model are shown in Table 1. The performance of the proposed system is tested under two case studies including all modes of operations. The case studies which are summarized in Table 2 are formed by varying the irradiation of PV and loading.

Table 1. Parameters of the proposed MPC						
System	Parameters	Value				
	PV maximum power (1000W/m ²)	3197 W				
ΡV	Maximum Power Point Voltage (1000W/m ²)	87 V				
	Maximum Power Point Current (1000W/m ²)	36.75 A				
	PV maximum power (250W/m ²)	801.3 W				
	Maximum Power Point Voltage (250W/m ²)	86.9 V				
	Maximum Power Point Current (250W/m ²)	9.221A				
Battery	Battery Capacity	12 Ah				
	Battery Nominal Voltage	220 V				
	Battery Maximum Charge Current	6 A				
Switched Capacitor	Inductor (L _{dc})	1 mH				
	Capacitor (C ₁)	250 uF				
	Capacitor (C ₂)	250 uF				
	Capacitor (C ₃)	500 uF				
General	Half Bridge Capacitors (C_4, C_5)	2 mF				
	DC-Link Capacitor (Cout)	1 mF				
	Nominal DC-Link Voltage	200				
	Switching Frequency	20 kHz				
	HET Turns Ratio	1.2.2				

 Table 2. Details of case studies

	Case 1 Time Intervals		Case 2 Time Intervals			
Time	0-0.25 s	0.25-0.5 s	0.5-0.75 s	0.75-1 s	0-0.25 s	0.25-0.5 s
Irradiation	300 W/m ²	650 W/m ²	1000 W/m ²	1000 W/m ²	-	-
Celcius	25 C°	25 C°	25 C°	25 C°	-	-
DC-Link Demand	1500 W	1500 W	1500 W	1500 W	-2000 W	-2000 W
Battery Charge/Discharge	Discharge	Charge	Charge (CC)	Charge (CV)	Charge (CC)	Charge (CV)
Mode	Mode 1	Mode 2	Mode 2	Mode 2	Mode 3	Mode 3



Figure 3. Performance results for case 1

The first case includes operating conditions in which mode 1, mode 2, and all control loops (MPPT, CC, CV, DC-link) are active, as well as the state responses of the transitions between these modes and loops. The different time intervals correspond to different operation modes and active control loops. Figure 3 shows the steady-state and dynamic responses of the proposed system during loop and mode transitions. It can be seen from the results that when the instantaneous power value of PV is lower than the load demand, the battery buffers and provides uninterrupted power to load. Following the raise in irradiation, the battery switches to charging in parallel with the increase in instantaneous power value of PV. During the state that the PV has relatively low output power, the MPPT control loop regulates the power flow from PV and the battery is charged with excessive power. However, in the case of high power generation from PV, the CC control loop is activated and the battery current is restricted. Following the battery voltage reaches a certain value, the CV control loop begins and keeps the output voltage of PV at a certain value. It is seen that the power of PV is increased to 1 kW, 2 kW, and 3 kW with the raise in irradiation, while the battery is first discharged and then charged to the maximum voltage value.

The second case represents mode 3 operation in which the battery is charged by the DC microgrid. Figure 4 shows the steady-state and dynamic responses of the proposed converter during reverse power flow conditions. In this case study, the battery is charged with a constant current during the time interval of 0-0.5 s. The charge current of the battery is adjusted by the CC control loop. The CC charging is followed by CV charging during the time interval of 0.5-1 s.

The performance waveforms highlight that the proposed MPC and controller smoothly regulates the steady-state and transient power-sharing during different irradiation and loading variations. Besides, the efficiency of the proposed converter has been also evaluated. As can be seen from Figure 3-4, the efficiency values considering the operating modes are 96%, 96.8%, and 90%, respectively. The efficiency values of the proposed converter show its effectiveness under all possible power flow conditions.



5. CONCLUSION AND DISCUSSION

This study proposes a novel isolated bidirectional multi-port converter (MPC) based on a switched-capacitor converter and a half-bridge converter with an effective control scheme. The main advantages of the proposed converter are as follows: (i) low voltage PV integration with high voltage gain and high-efficiency switched-capacitor DC-DC converter, (ii) battery integration without an additional converter, (iii) reduced switch topology, (iv) cost-effectiveness (v) bidirectional power flow capability. The effectiveness of the proposed MPC is evaluated under various operating conditions. The results show that the proposed MPC performs all the aforementioned functionalities. In addition to its satisfactory performance under steady-state conditions, it provides effective power transfer with smooth transitions between control loops during transient conditions.

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