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THE HARMONIC ELIMINATION IN INVERTERS WITH METAHEURISTIC APPROACHES

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Abstract: Inverters which convert DC voltage into AC voltage are widely used in the field of power electronics. During the conversion from DC voltage to AC voltage, some of the harmonics can occur. Harmonics which are one of the most important parameters in the field of energy/power systems determine the power quality. Therefore, one of the crucial steps is the elimination/suppression of harmonics in the outputs of the inverters. It is required to solve complex equations with multiple variables in this elimination process. In this study, a software is developed and eight different metaheuristic algorithms/approaches (backtracking search, cuckoo search, electromagnetic field optimization, harmony search, Harris hawks optimization, interior search, vortex search, Yin-Yang-pair optimization) are used for the elimination of the chosen harmonics by determining suitable switching angles. The desired harmonics are eliminated in the chosen inverter circuits via the software with a user-friendly interface easily, fast and effectively, and the results can be observed both numerically and graphically through the this software.

Keywords: Inverter, harmonic elimination, metaheuristic algorithms.

Metasezgisel Yaklaşımlarla Eviricilerde Harmonik Eliminasyonu

Öz: DC gerilimi AC gerilime dönüştüren eviriciler (invertörler), güç elektroniği alanında en çok kullanılan devre türlerinin başında gelmektedir. Anahtarlamalarla DC gerilimden AC gerilime dönüşüm esnasında, çıkış geriliminde harmonikler oluşmaktadır. Harmonikler ise enerji/güç sistemlerinde güç kalitesini belirleyen parametrelerin başında yer almaktadır. Dolayısıyla evirici devrelerinin çıkışındaki harmoniklerin elenmesi/bastırılması önem arz etmektedir. Bu eleme işleminde ise çok değişkenli karmaşık denklem sistemlerinin çözülmesi gerekmektedir. Gerçekleştirilen çalışmada, seçilen harmonikleri elemek için en uygun anahtarlama açılarını, sekiz farklı metasezgisel algoritma/yaklaşım (geri izleme arama, guguk kuşu arama, elektromanyetik alan optimizasyonu, harmoni arama, Harris şahinleri optimizasyonu, dahili/iç arama, girdap arama, Yin-Yang-çifti optimizasyonu) ile hesaplayan yazılım geliştirilmiştir. Kullanıcı dostu arayüze sahip yazılım ile seçilen türdeki evirici devrelerinde, belirlenen harmonikler hızlı, kolay ve etkin bir şekilde elenebilmekte; sonuçlar hem sayısal hem de grafiksel olarak gözlenebilmektedir.

Anahtar Kelimeler: Evirici, harmonik eliminasyon, metasezgisel algoritmalar.

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1. INTRODUCTION

In the past decade, the usage of power electronics circuits/systems has been increased with the developments of industry. One of these systems is the inverters that convert energy. These systems have been widely used in many areas, such as flexible AC transmission system (FACTS) devices, high voltage direct current lines, electrical drives etc. and can also be used for an energy conversion as the inverters. Performances of the systems which convert DC voltage to AC voltage can be determined depend on harmonics at output voltage. There are many different techniques and applications to eliminate the harmonics at the output. Pulse width modulation (PWM) control is one of the important techniques based on the calculation/optimization of switching angles of inverter for the desired output voltage (Ahmadi et al., 2011; Amjad and Salam, 2014; Babu et al., 2015; Dahidah and Agelidis, 2008; Dahidah et al., 2015; Kavousi et al., 2012; Liu et al., 2009). Calculation/optimization of the triggering angles of the switching elements for the output voltage of the desired characteristic forms the basis of this control. The solution of large systems of equations (linear/nonlinear) is required if the structure of the circuit becomes more complex, meaning that manual analytical solutions for such problems are extremely difficult. A wide various of problems can be solved quickly and easily with metaheuristic algorithms, therefore, these algorithms can be utilized effectively and efficiently in these optimization processes.

In this work, eight different metaheuristic algorithms, which are backtracking search (BS), cuckoo search (CS), electromagnetic field optimization (EFO), harmony search (HS), Harris hawks optimization (HHO), interior search (ISA), vortex search (VS), Yin-Yang-pair optimization (YYPO), are used to calculate switching angles by solving the related equations for the elimination of desired harmonics. The main aim of this study is to solve harmonic elimination problems, which are hard to solve analytically, within a reasonable process time and to find switching angles automatically and easily by combining a lot of metaheuristics with a software. Using the software developed for this purpose, undesired harmonics can be eliminated at the output and optimal switching angles can also be determined fast and effectively.

This paper is organized as follows: Inverters and the used metaheuristic algorithms are briefly described in Section 2-3. The designed simulator and applications (results) are given in Section 4 and finally, the conclusions are outlined in Section 5.

2. INVERTERS

The inverters or DC-AC inverters which are frequently used in the industry convert DC power into AC power in the desired frequency and amplitude (Fig 1). In theory, the output of the inverters is sinusoidal, but, in the real-world applications, it cannot be sinusoidal as it includes harmonics. Besides, the harmonics can be reduced by using the switching techniques with the high-speed semiconductor components used in the inverters.

Undesirable harmonics in single-phase inverters can be eliminated by using a unipolar or bipolar voltage chops. The both conditions are summarized in Table 1 (Gürdal, 2008; Patel and Hoft, 1973; Rashid, 2013).

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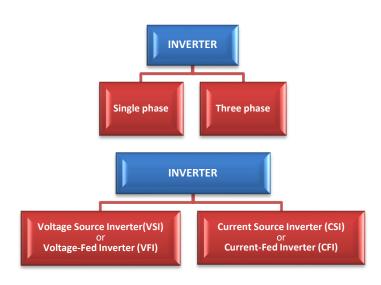
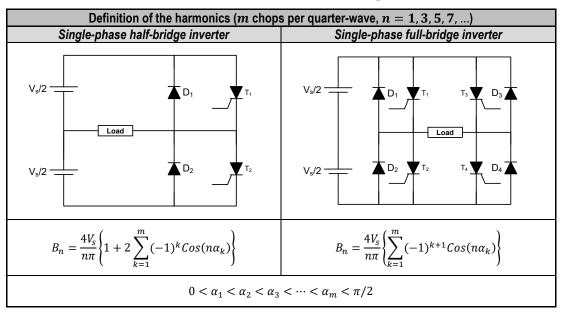


Figure 1: Types of inverters

3. METAHEURISTIC ALGORITHMS/APPROACHES

The use of metaheuristic algorithms in many areas has been rapidly increased with the developments of the technologies in recent years. Especially, these algorithms are frequently used in the area of optimization related to social and engineering sciences in solving optimization problems. The flowcharts of the algorithms used in this work are given in Table 2 (Abedinpourshotorban et al., 2016; Civicioglu, 2013; Dogan and Olmez, 2015; Gandomi, 2014; Geem et al., 2001; Heidari et al., 2019; Punnathanam and Kotecha, 2016; Yang, 2009; Yang and Deb, 2009).

 Table 1. The harmonics at the output



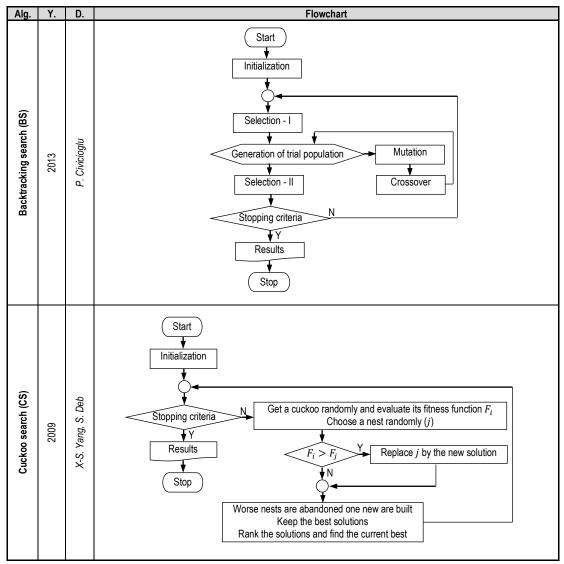
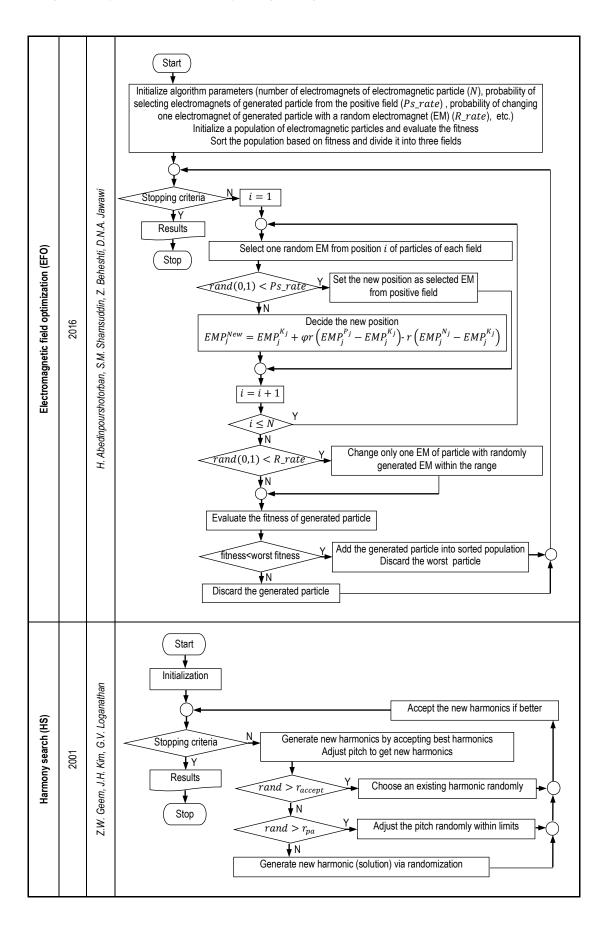
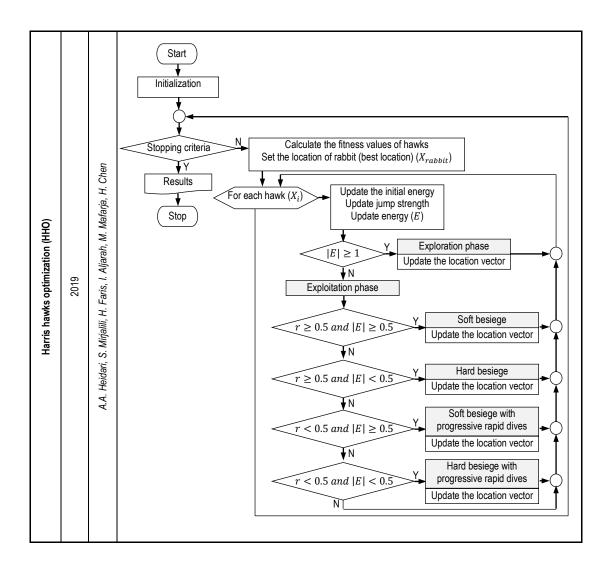
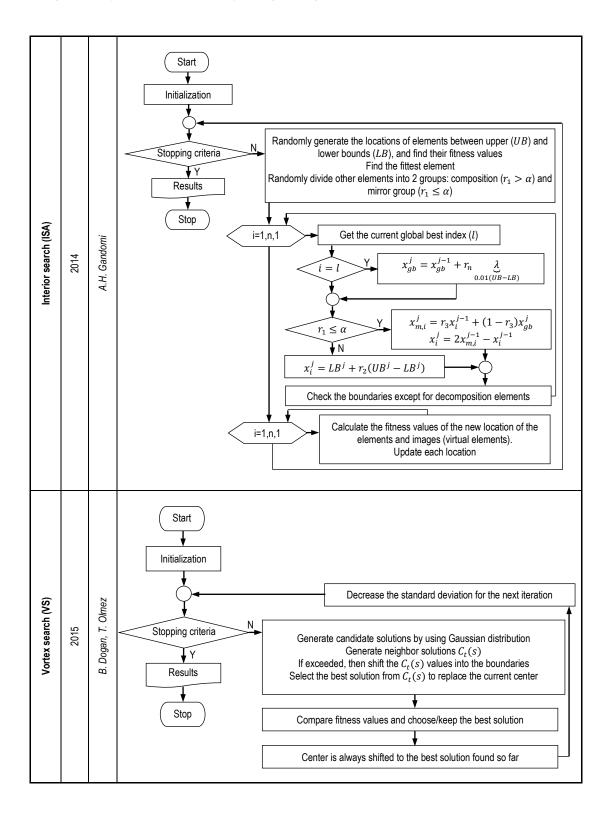
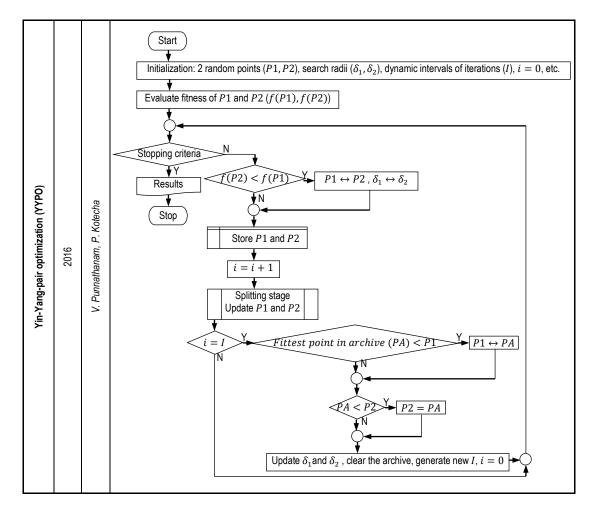


Table 2. The flowcharts of the used algorithms







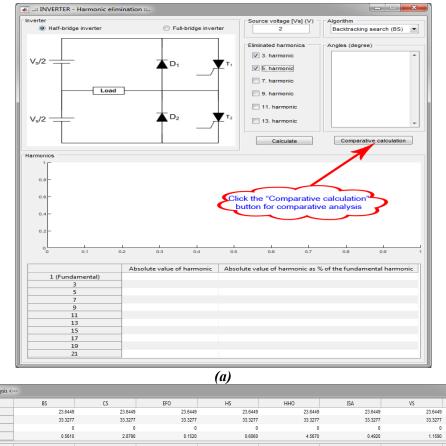


4. APPLICATIONS

In this work, MATLAB (MathWorks, 2019) is used as a graphical user interface to perform the applications. Using the metaheuristic algorithms, single or comparative analysis can be carried out to determine the optimal angles through the software developed in order to eliminate harmonics. The software is also able to show the results graphically and numerically. In the first application, the purpose is to eliminate third and fifth harmonics in the half-bridge inverter and the related equations are given in Eq. 1 (Rashid, 2013). The comparative results related to the solution of Eq. 1 can be shown in Figure 2 and Table 3.

$$1 - 2Cos(3\alpha_1) + 2Cos(3\alpha_2) = 0 1 - 2Cos(5\alpha_1) + 2Cos(5\alpha_2) = 0$$
 (1)

It can be seen that in Table 3, there are no big differences (less than %0.001) in terms of the values of both angles α_1 and α_2 for all the algorithms. However, considering run-times given in the last column of Table 3, all the algorithms solve the harmonic elimination problem with different speeds. The table indicates that EFO achieves the results with the lowest run-time of 0.152 s. ISA and YYPO stay behind EFO with a difference of around 0.3 s. In addition to this, HHO, which is the more current algorithm than the others, but the run-time of this algorithm is considerably low as compared to the rest of the algorithms for the first application. HS and BS have also the similar run-time with the identical angle values, showing that one of them can be chosen in solving this problem.



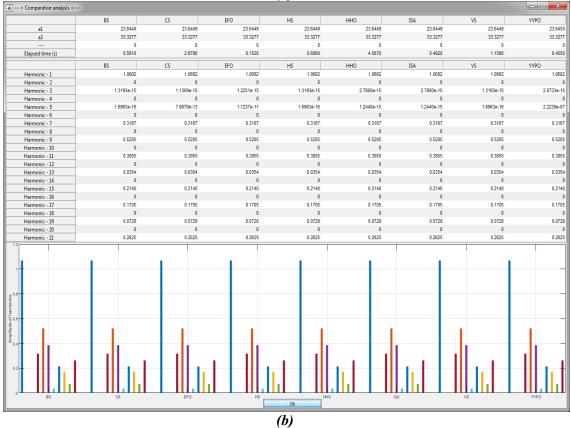


Figure 2: The screenshot of the first application

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Algorithm	α ₁	α_2	Time (s)
BS	23.6449441898361	33.3276795599481	0.561
CS	23.6449441898099	33.3276795599229	2.879
EFO	23.6449441894526	33.3276795595800	0.152
HS	23.6449441898361	33.3276795599481	0.606
нно	23.6449441898361	33.3276795599480	4.567
ISA	23.6449441898361	33.3276795599480	0.492
VS	23.6449441898361	33.3276795599481	1.159
YYPO	23.6449517792410	33.3276868433341	0.455

Table 3. The results of the first application

In the second applications, the half-bridge inverter is used for elimination of fifth and seventh harmonics (Patel and Hoft, 1973). The singular and comparative results of second application are given in Figure 3 and Table 4, respectively. When regarding the outcomes of the switching angles given in Table 4, all the algorithms achieve similar results, which are almost the degrees of 16 and 22 for α_1 and α_2 , respectively. When comparing the first application with the second one in terms of run-time, EFO remains stable at almost the run-time of 0.15 s while a sharp increase occurs in HS, rising up from 0.606 s in the first application to 1.524 s in the second application. In contrast, YYPO, which is in the second place with respect to the run-time, can reach similar results without any clear differences. These show that EFO and YYPO are performing well for both the applications. Moreover, the highest run-time belongs to HHO as similar to the first application. This indicates that HHO is not suitable in the applications which require high speed.

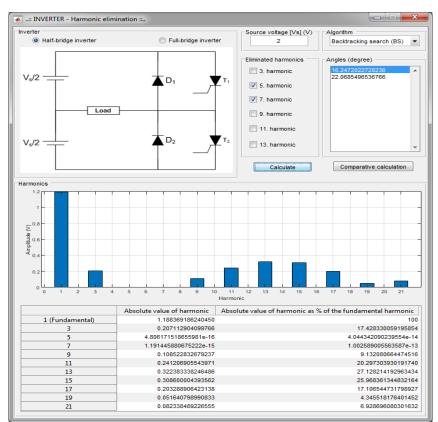


Figure 3: The screenshot of the second application

Algorithm	α1	α2	Time (s)
BS	16.2472022720236	22.0685496536766	0.448
CS	16.2472022730367	22.0685496547445	3.572
EFO	16.2472021397706	22.0685495142732	0.159
HS	16.2472022720236	22.0685496536766	1.524
нно	16.2472022720200	22.0685496536700	5.990
ISA	16.2472022720236	22.0685496536766	0.509
VS	16.2472022720236	22.0685496536766	1.132
YYPO	16.2470447379089	22.0683836028402	0.448

Table 4. The results of the second application

In the third application, the full-bridge is used for the elimination of fifth and seventh harmonics (Patel and Hoft, 1973). The singular and comparative results of third application are given in Figure 4 and Table 5, respectively. As shown from Table 5, as similar to the first and second applications, no significant differences exist between the algorithms in terms of the values of angles α_1 and α_2 . The differences become when considering the run-time of the algorithms, demonstrating that EFO is still in the first place with 0.162 s. as compared to the other algorithms. HHO and CS achieve the worst run-times, meaning that these algorithms should not be in the first choice if run-time is substantially important. On the other hand, BS and ISA have very close run-times and outperform the other algorithms, except for EFO and YYPO, showing the capabilities of them in finding the switching angles within reasonable run-times. It can be observed from the outcomes of all the applications that the problems can be solved with these eight algorithms, but if the run-times of the algorithms are taken into account, EFO is the best option as compared with the other algorithms for all the applications.

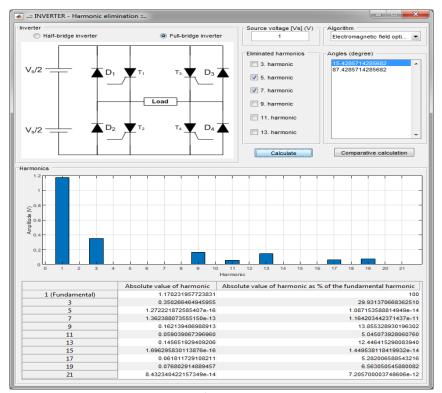


Figure 4: The screenshot of the third application

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Algorithm	α ₁	α_2	Time (s)
BS	15.4285714285714	87.4285714285714	0.472
CS	15.4285714285714	87.4285714285714	3.452
EFO	15.4285714285682	87.4285714285682	0.162
HS	15.4285856118238	87.4285856118238	0.782
ННО	15.4285714285714	87.4285714285714	4.768
ISA	15.4285714285714	87.4285714285714	0.428
VS	15.4285714285714	87.4285714285714	1.110
YYPO	15.4286030173399	87.4286030173399	0.366

Table 5. The results of the third application

5. CONCLUSION

The inverters are used for a wide variety of applications due to its flexibility and ease of use in the field of power electronics. In this work, eight metaheuristic algorithms called BS, CS, HS, VS, EFO, ISA, YYPO and HHO are used to eliminate harmonics and three applications are implemented. To do this, the software having the ability to effectively handle harmonic elimination problems is designed, showing the related results numerically and graphically to the users. According to the results, EFO is the fastest algorithm for all the applications, proving itself to be the best option in terms of run-time. YYPO and ISA also stay in second and third places, respectively. If small differences of run-times between these algorithms are not taken into account, all the applications can be performed successfully with them. The results show that the algorithms used can eliminate harmonics fast and easily, and have sufficient capacity in finding optimal switching angles for all the applications, but with different speeds. Further works should be extended to multilevel inverters to compare the performances of the algorithms.

REFERENCES

- 1. Abedinpourshotorban, H., Shamsuddin, S.M., Beheshti, Z., Jawawi, D.N.A. (2016) Electromagnetic field optimization: A physics-inspired metaheuristic optimization algorithm, *Swarm and Evolutionary Computation*, 26, 8-22. doi: 10.1016/j.swevo.2015.07.002.
- 2. Ahmadi, D., Zou, K., Li, C., Huang, Y., Wang, J. (2011) A universal selective harmonic elimination method for high-power inverters, *IEEE Transactions on Power Electronics*, 26 (10), 2743-2752. doi: 10.1109/TPEL.2011.2116042.
- **3.** Amjad, A.M., Salam, Z. (2014) A review of soft computing methods for harmonics elimination PWM for inverters in renewable energy conversion systems, *Renewable and Sustainable Energy Reviews*, 33, 141-153. doi: 10.1016/j.rser.2014.01.080.
- 4. Babu, T.S., Priya, K., Maheswaran, D., Kumar, K.S., Rajasekar, N. (2015) Selective voltage harmonic elimination in PWM inverter using bacterial foraging algorithm, *Swarm and Evolutionary Computation*, 20, 74-81. doi: 10.1016/j.swevo.2014.11.002.
- 5. Civicioglu, P. (2013) Backtracking search optimization algorithm for numerical optimization problems, *Applied Mathematics and Computation*, 219, 8121-8144. doi: 10.1016/j.amc.2013.02.017.
- 6. Dahidah, M.S.A., Agelidis, V.G. (2008) Selective harmonic elimination PWM control for cascaded multilevel voltage source converters: a generalized formula, *IEEE Transactions on Power Electronics*, 23 (4), 1620-1630. doi: 10.1109/TPEL.2008.925179.

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- 7. Dahidah, M.S.A., Konstantinou, G., Agelidis, V.G. (2015) A review of multilevel selective harmonic elimination PWM: formulations, solving algorithm, implementation and applications, *IEEE Transactions on Power Electronics*, 30 (8), 4091-4106. doi: 10.1109/TPEL.2014.2355226.
- Dogan, B., Olmez, T. (2015) A new metaheuristic for numerical function optimization: vortex search algorithm, *Information Sciences*, 293, 125-145. doi: 10.1016/j.ins.2014.08.053.
- 9. Gandomi, A.H. (2014) Interior search algorithm (ISA): A novel approach for global optimization, *ISA transactions*, 53(4), 1168-1183. doi: 10.1016/j.isatra.2014.03.018.
- 10. Geem, Z.Y., Kim, J.H., Loganathan, G.V. (2001) A new heuristic optimization algorithm: harmony search, *Simulation*, 76 (2), 60-68. doi: 10.1177/003754970107600201.
- 11. Gürdal, O. (2008) Güç elektroniği, Seçkin Yayıncılık, Ankara.
- 12. Heidari, A.A., Mirjalili, S., Faris, H., Aljarah, I., Mafarja, M., Chen, H. (2019) Harris hawks optimization: Algorithm and applications, *Future Generation Computer Systems*, 97, 849-872. doi: 10.1016/j.future.2019.02.028.
- **13.** Kavousi, A., Vahidi, B., Salehi, R., Bakhshizadeh, M.K., Farokhnia, N., Fathi, S.H. (2012) Application of the bee algorithm for selective harmonic elimination strategy in multilevel inverters, *IEEE Transactions on Power Electronics*, 27 (4), 1689-1696. doi: 10.1109/TPEL.2011.2166124.
- 14. Liu, Y., Hong, H., Huang, A.Q. (2009) Real-time calculation of switching angles minimizing THD for multilevel inverters with step modulation, *IEEE Transactions on Industrial Electronics*, 56 (2), 285-293. doi: 10.1109/TIE.2008.918461.
- 15. MathWorks (2019). MATLAB. https://www.mathworks.com/.
- Patel, H.S., Hoft, R.G. (1973) Generalized techniques of harmonic elimination and voltage control in thyristor inverters: part I - harmonic elimination, *IEEE Transactions on Industry Applications*, vol. IA-9 (3), 310-317. doi: 10.1109/TIA.1973.349908.
- Punnathanam, V., Kotecha, P. (2016) Yin-Yang-pair Optimization: A novel lightweight optimization algorithm, *Engineering Applications of Artificial Intelligence*, 54, 62-79. doi: 10.1016/j.engappai.2016.04.004.
- **18.** Rashid, M.H. (2013) *Power Electronics: Circuits, Devices & Applications*, 4th ed., Pearson Education.
- **19.** Yang, X.-S. (2009) Harmony search as a metaheuristic algorithm, in: Music-inspired harmony search algorithm: theory and applications (Ed. Geem), Studies in Computational Intelligence, vol. 191, Springer, Berlin.
- **20.** Yang, X-S., Deb, S. (2009) Cuckoo search via Lévy flights, 2009 World Congress on Nature & Biologically Inspired Computing (NaBIC 2009), Coimbatore/India, 9-11 December. 210-214. doi: 10.1109/NABIC.2009.5393690.